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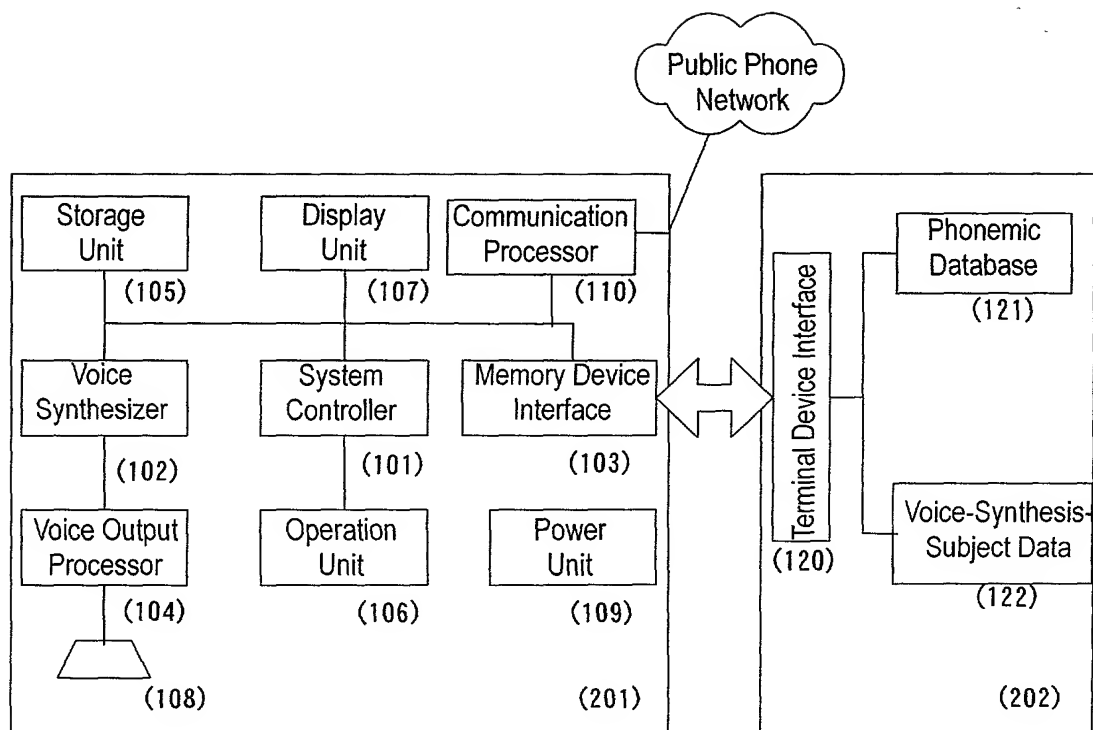


Fig. 2

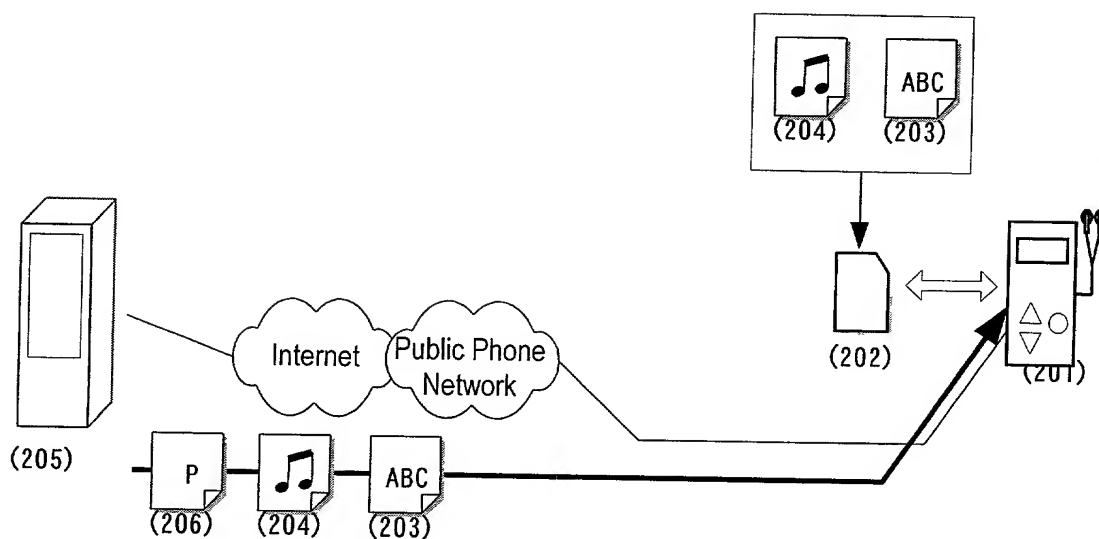


Fig. 3

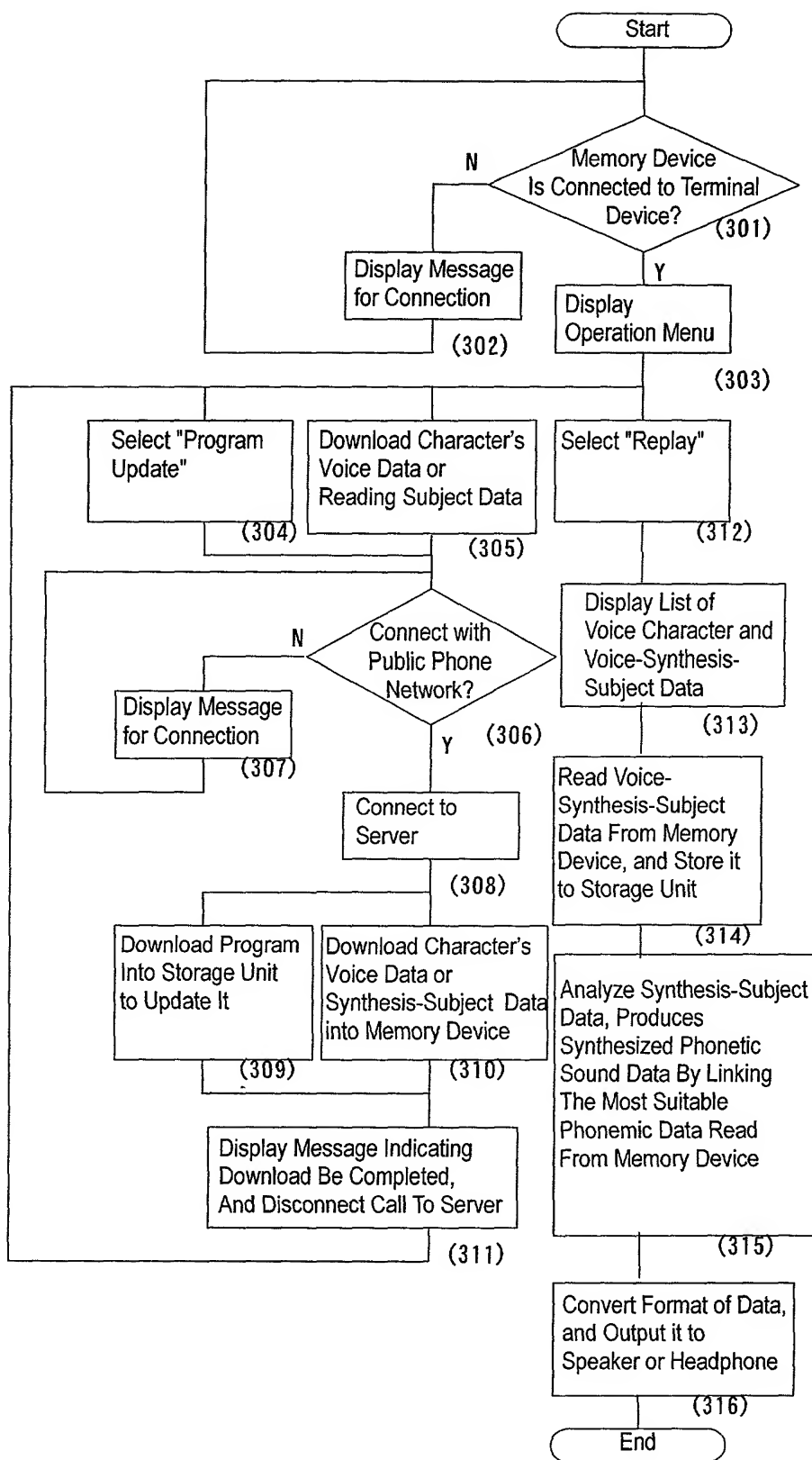


Fig. 4

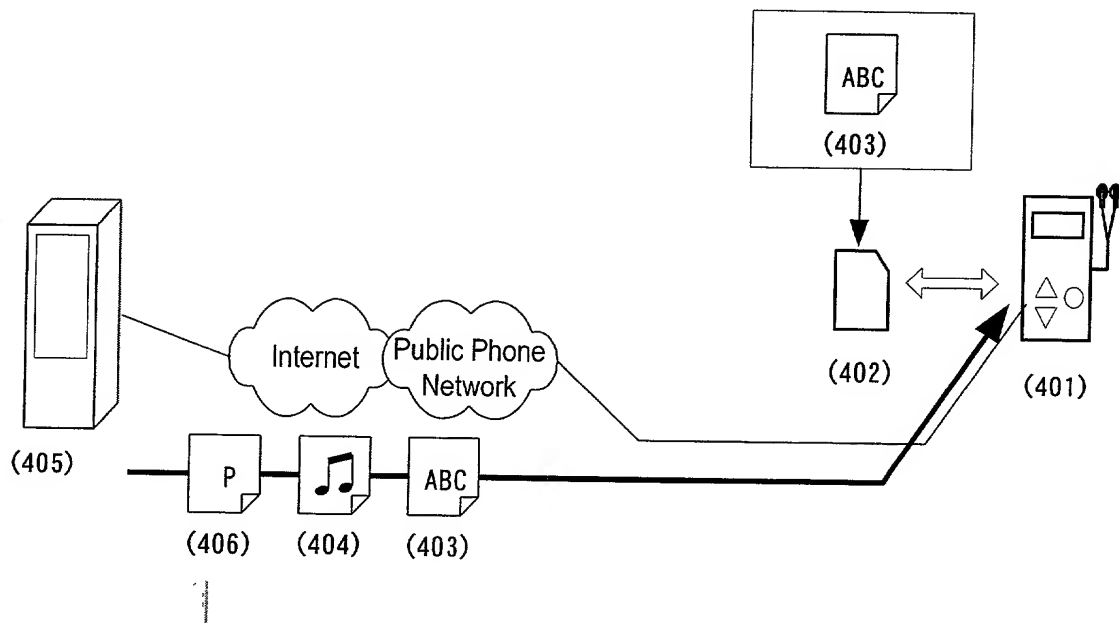


Fig. 5

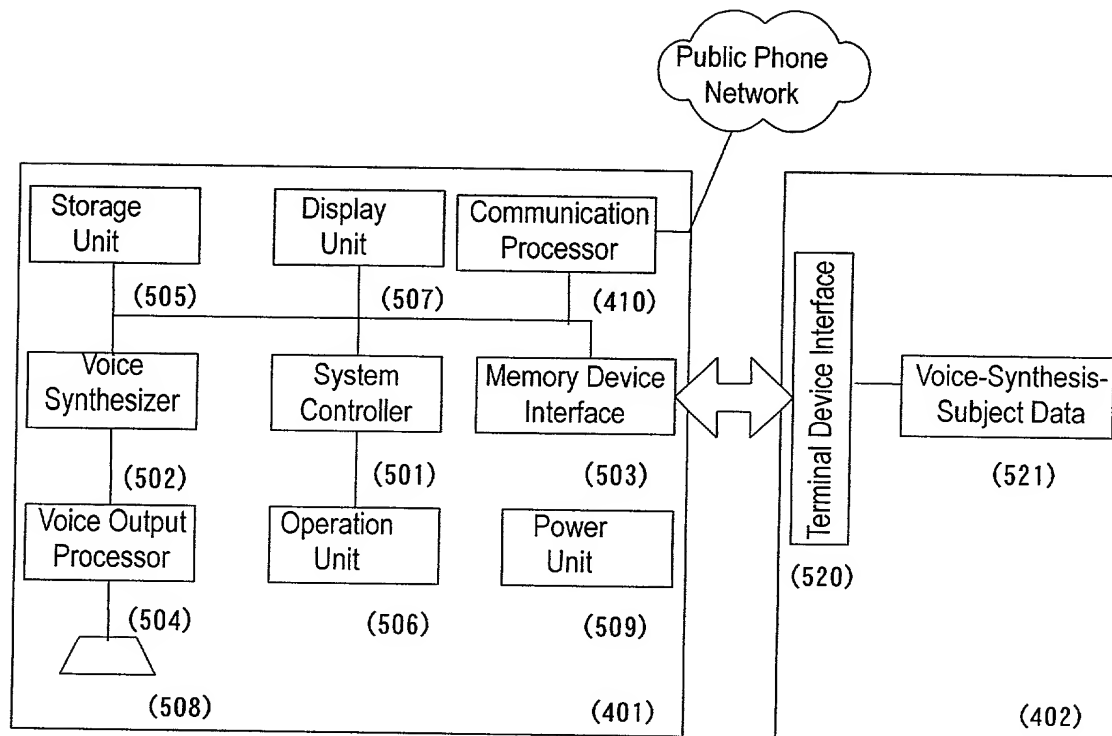


Fig. 6

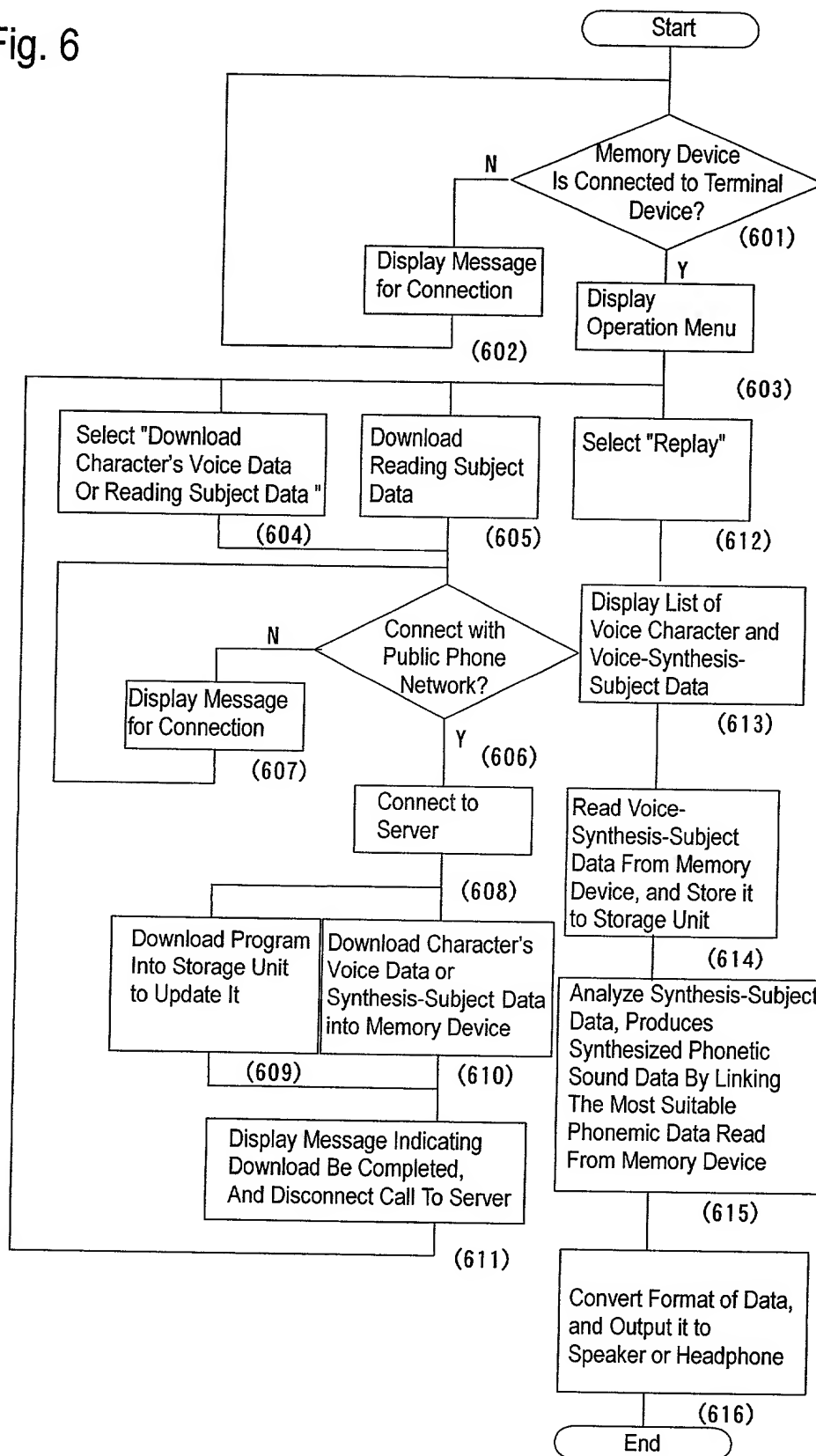


Fig. 7

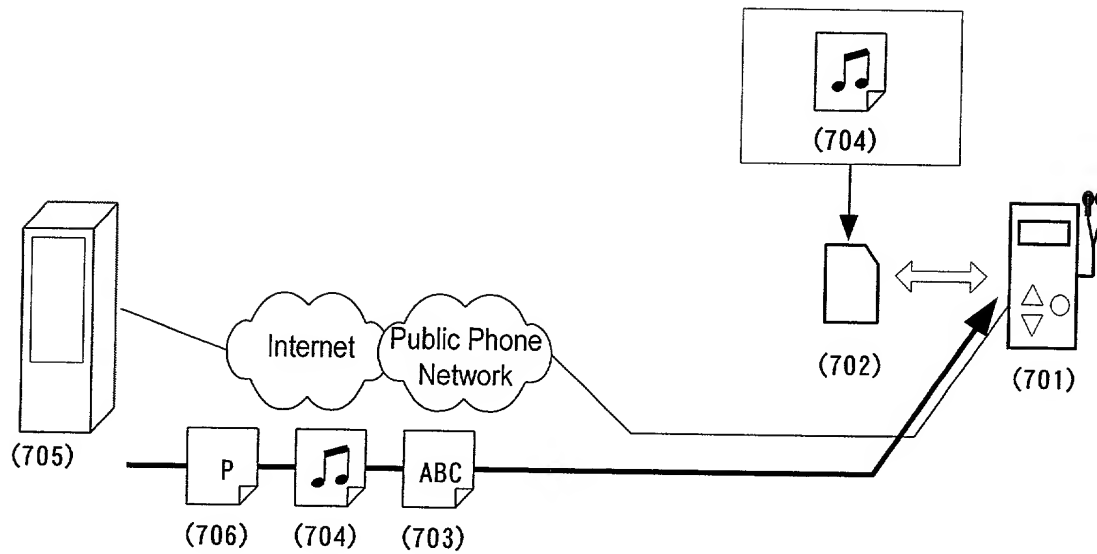


Fig. 8

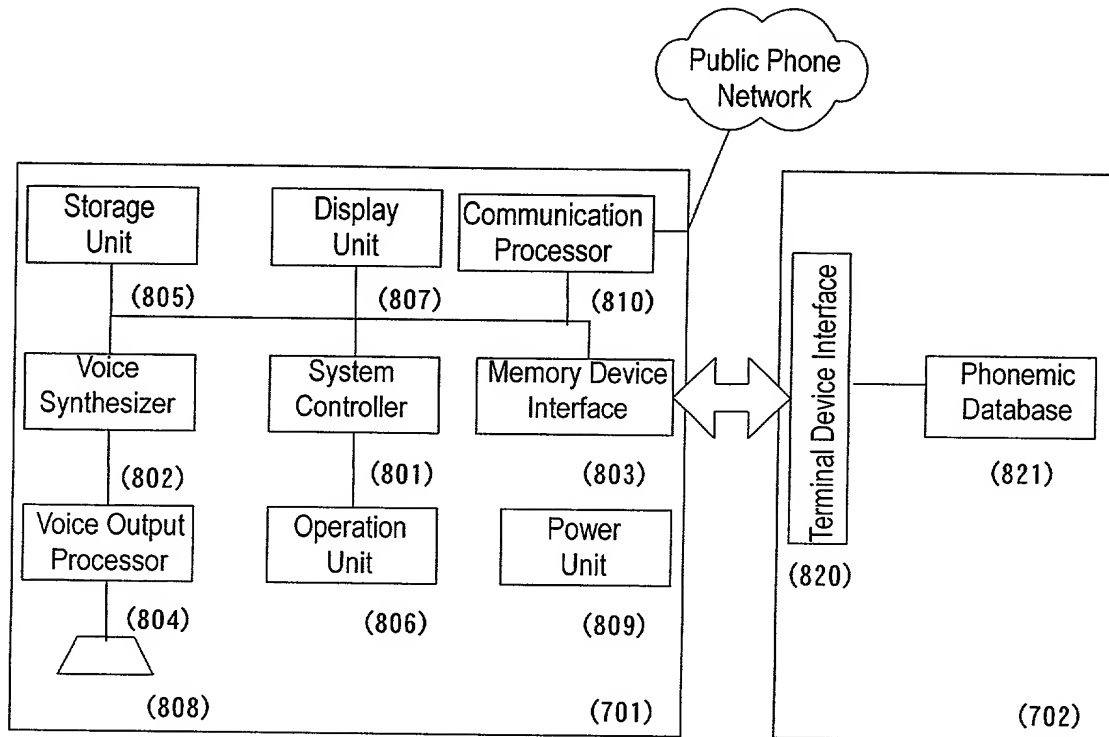


Fig. 9

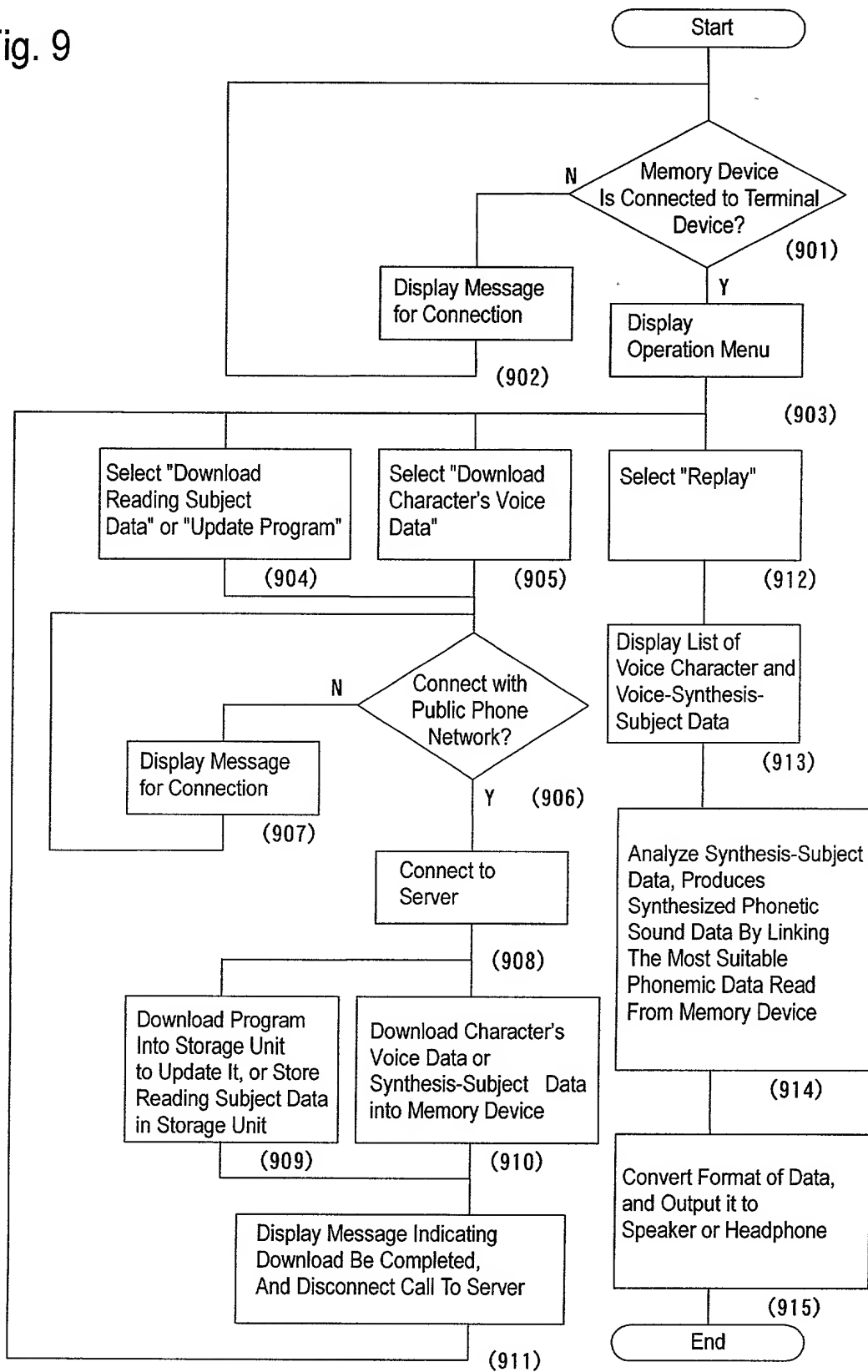


Fig. 10

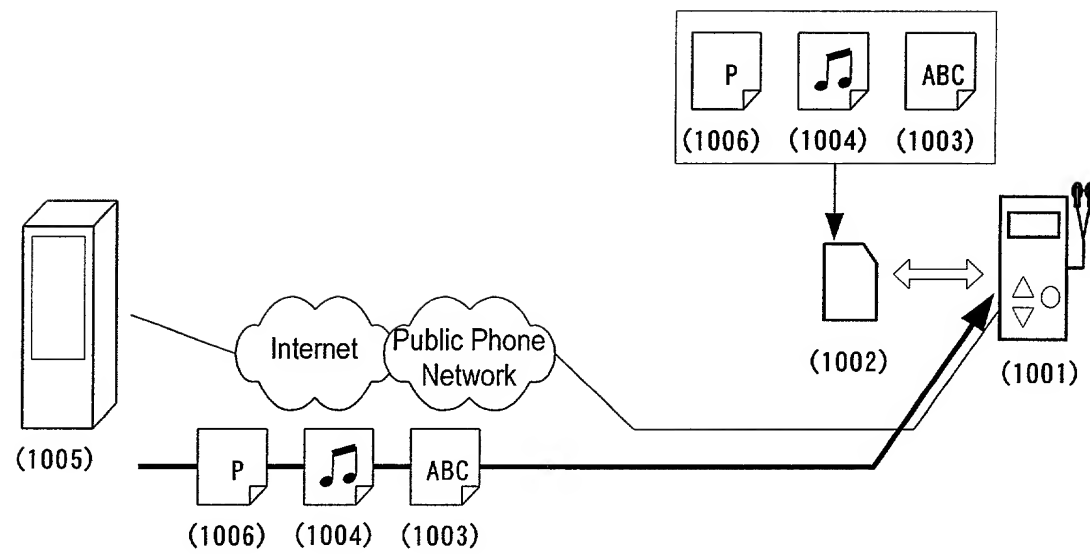


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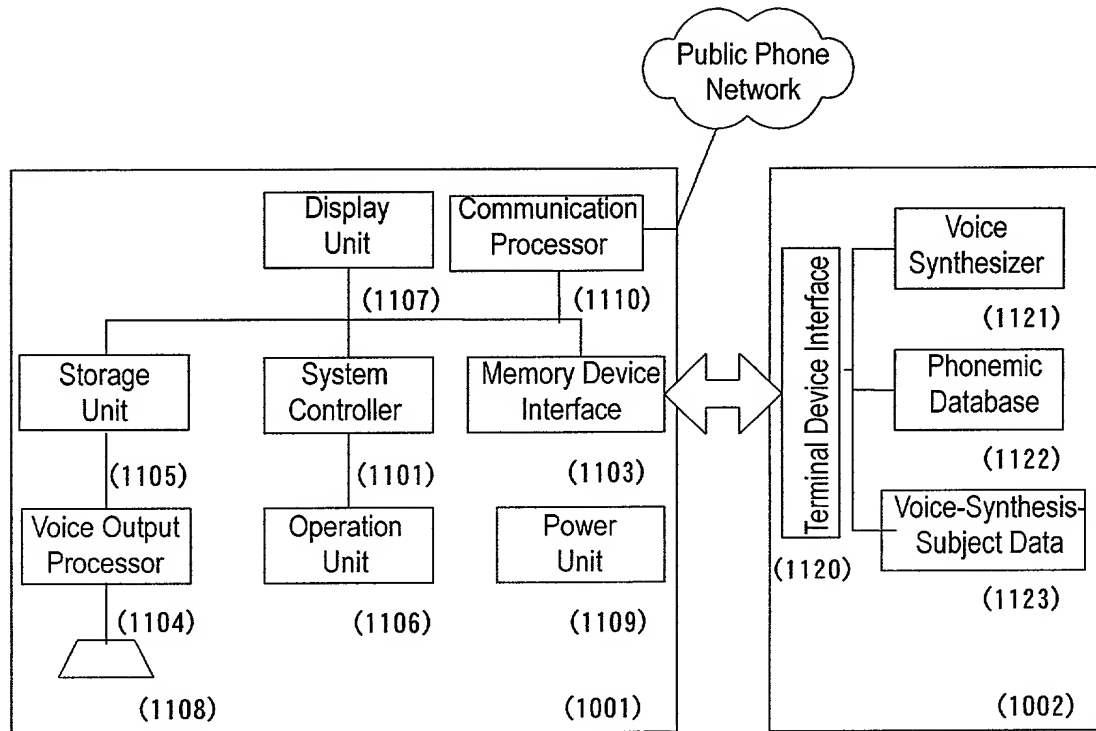


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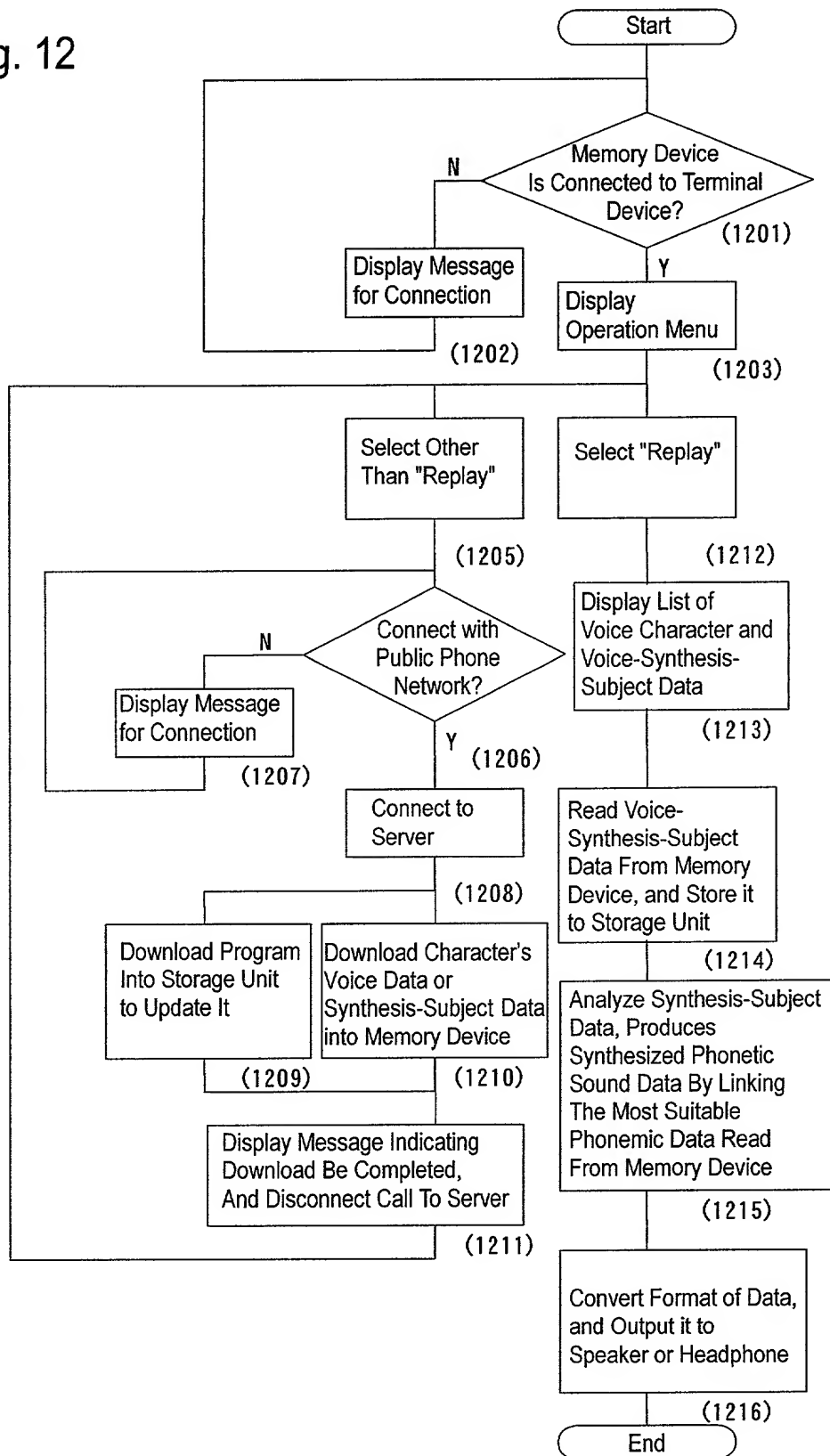


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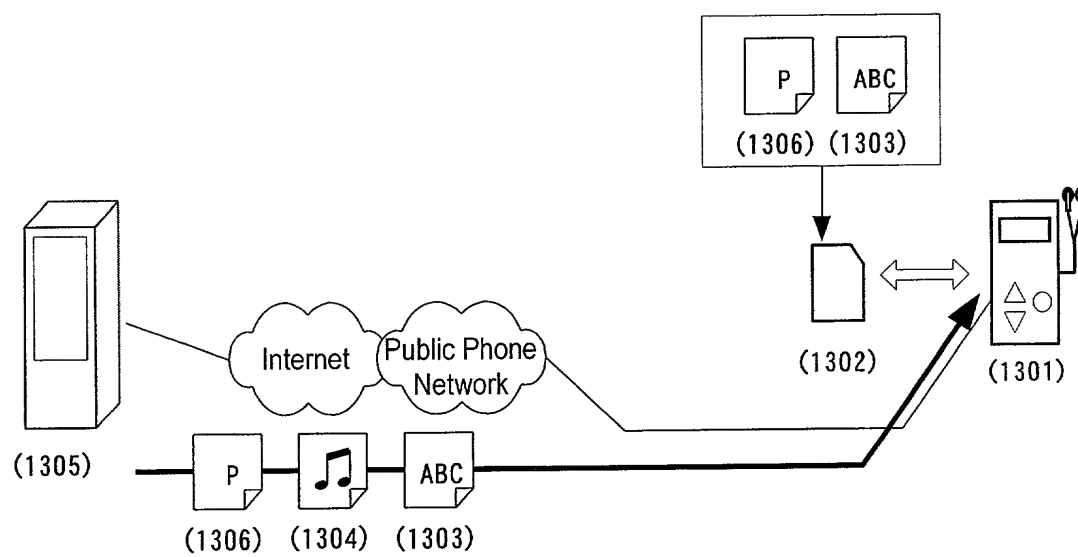


Fig. 14

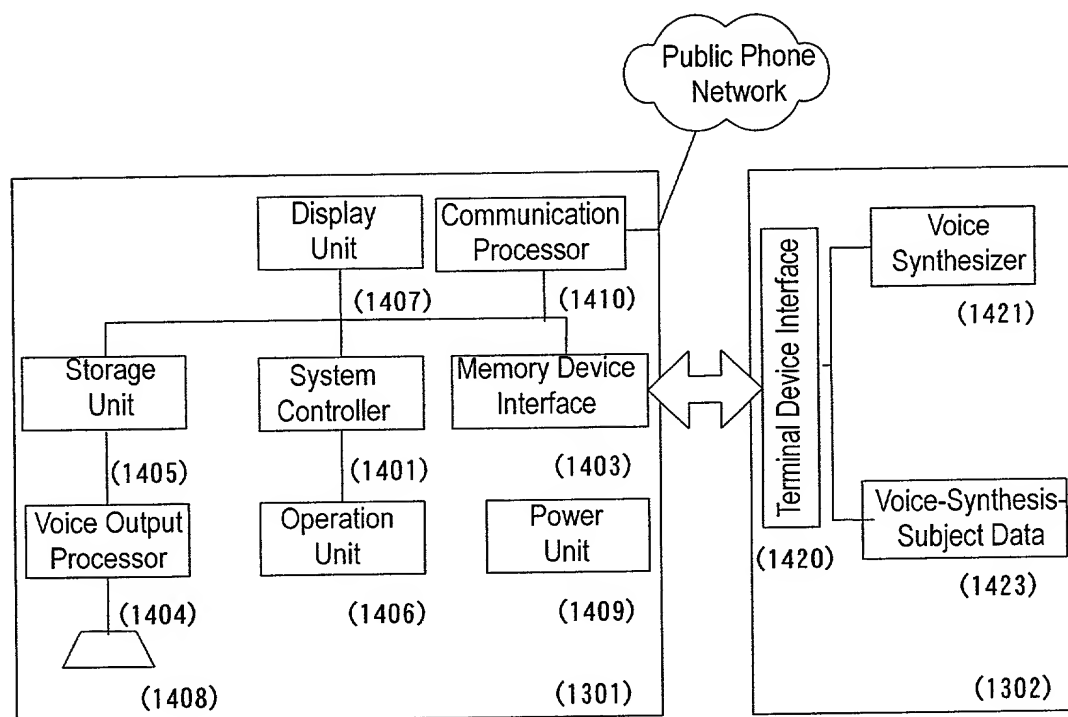


Fig. 15

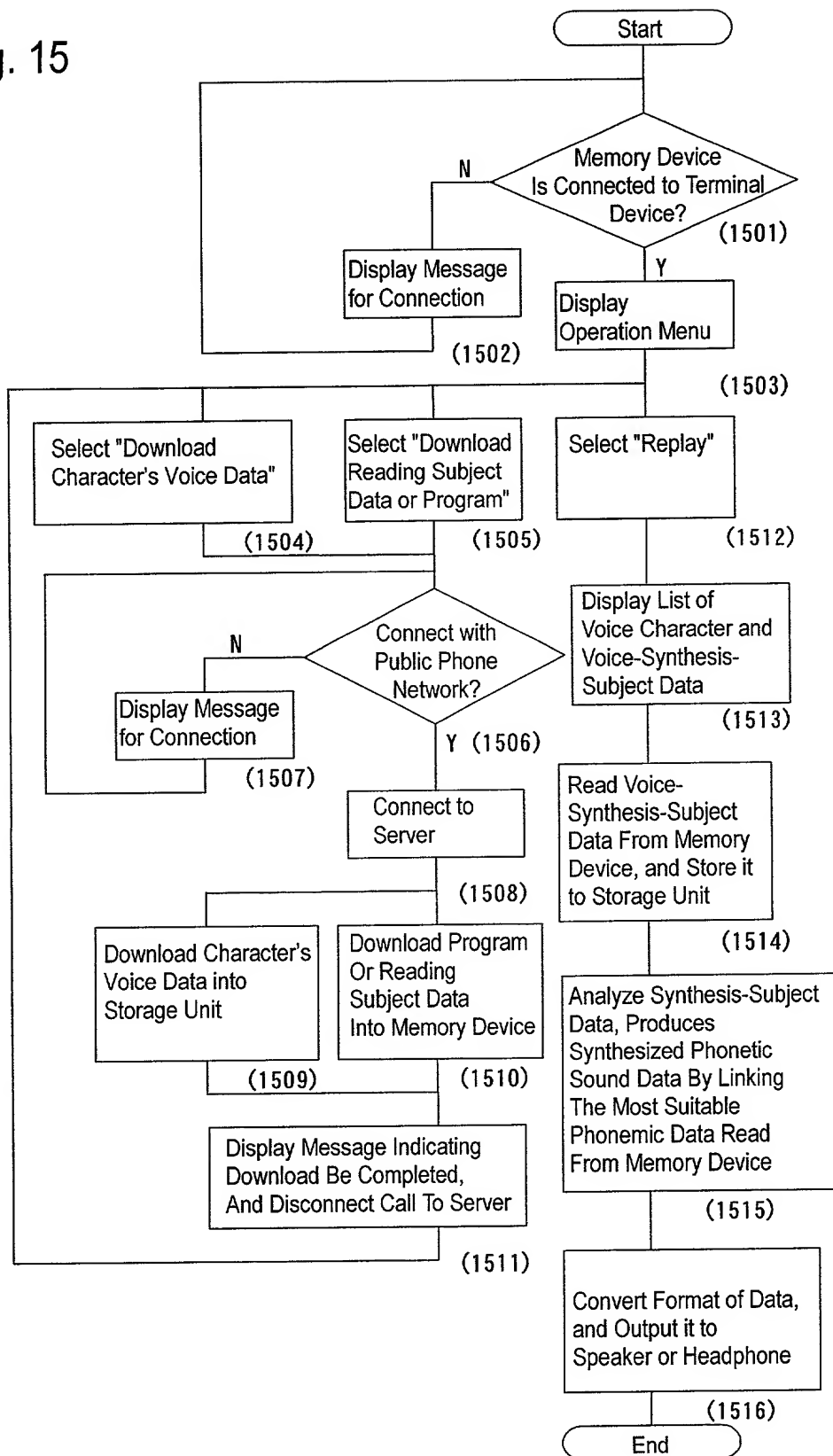


Fig. 16

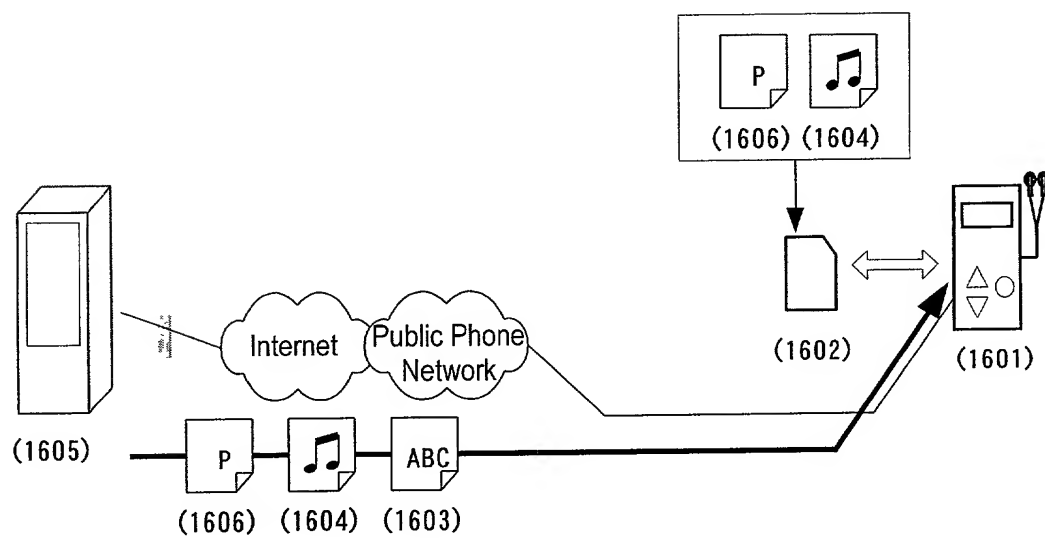


Fig. 17

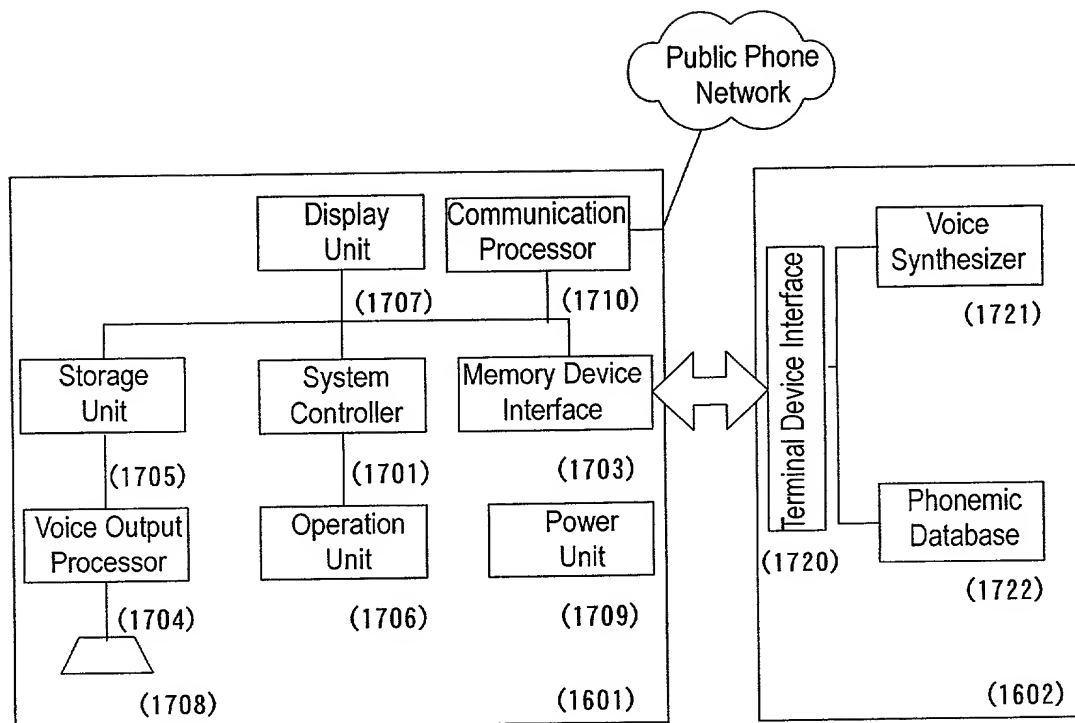


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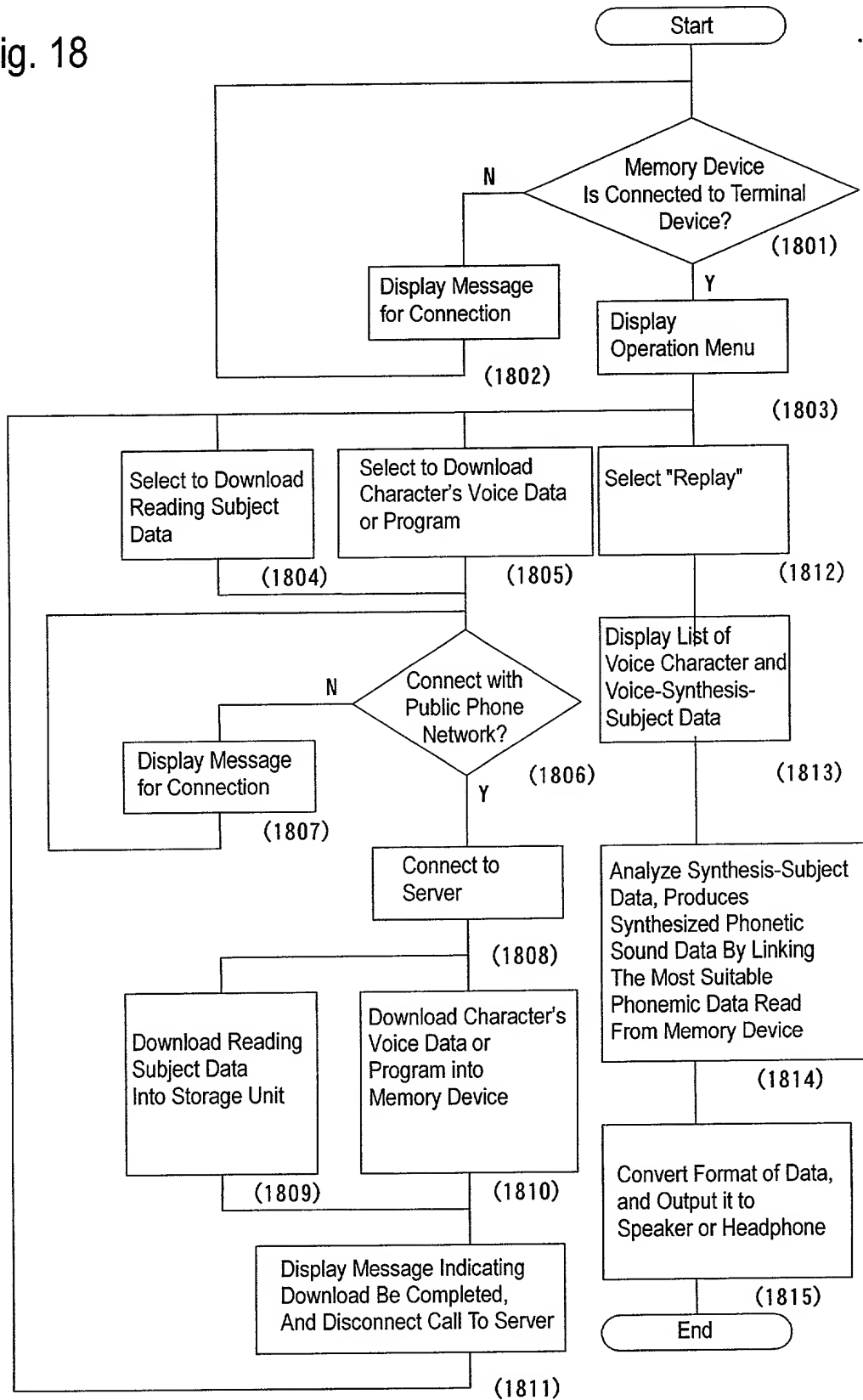


Fig. 19

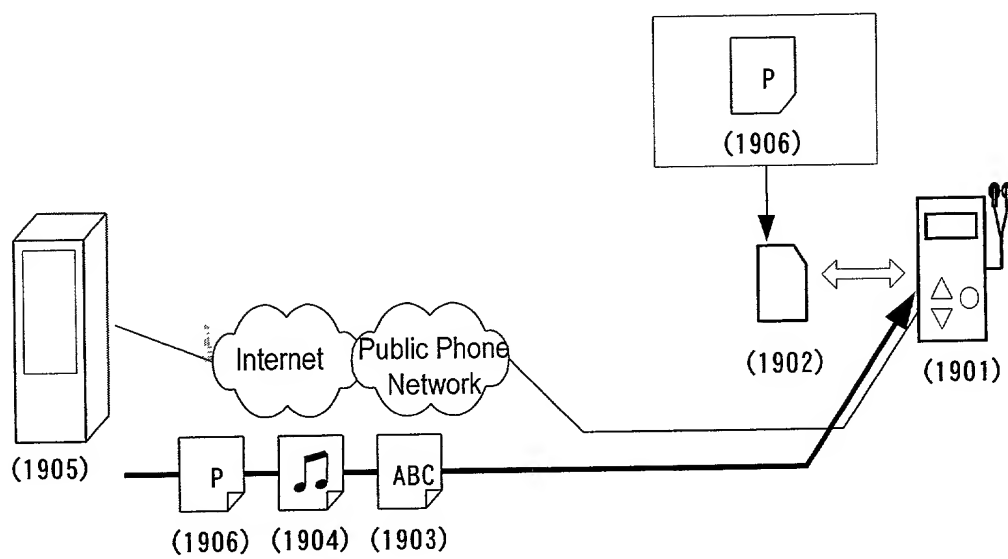


Fig. 20

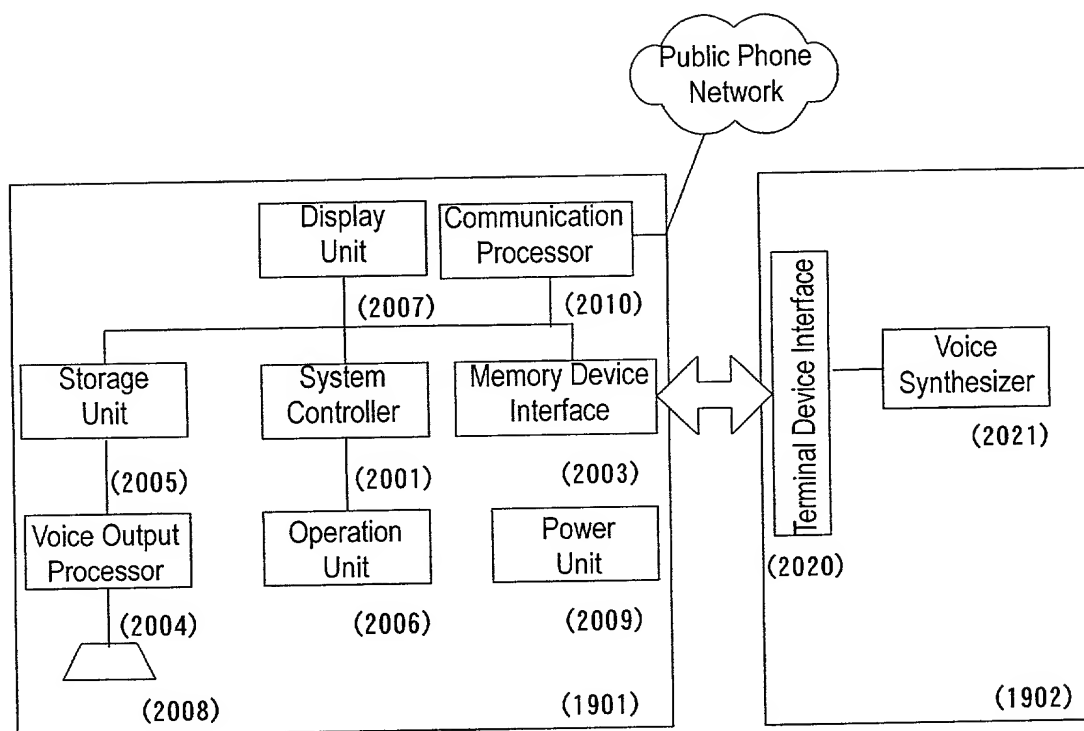


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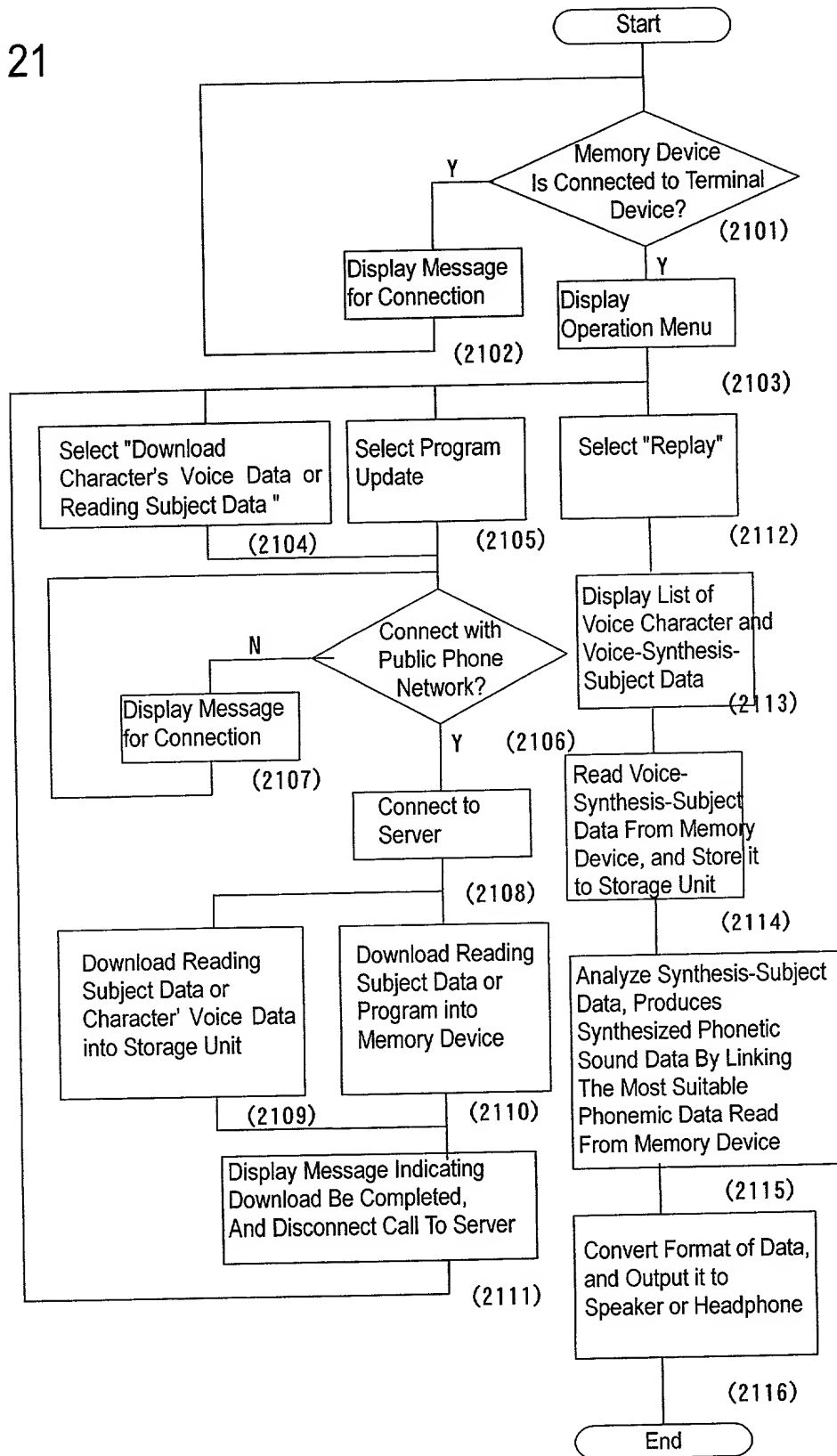


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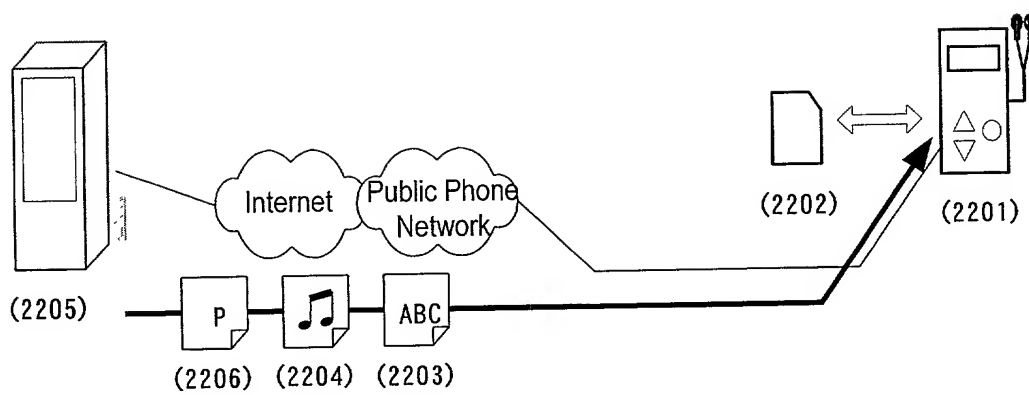


Fig. 23

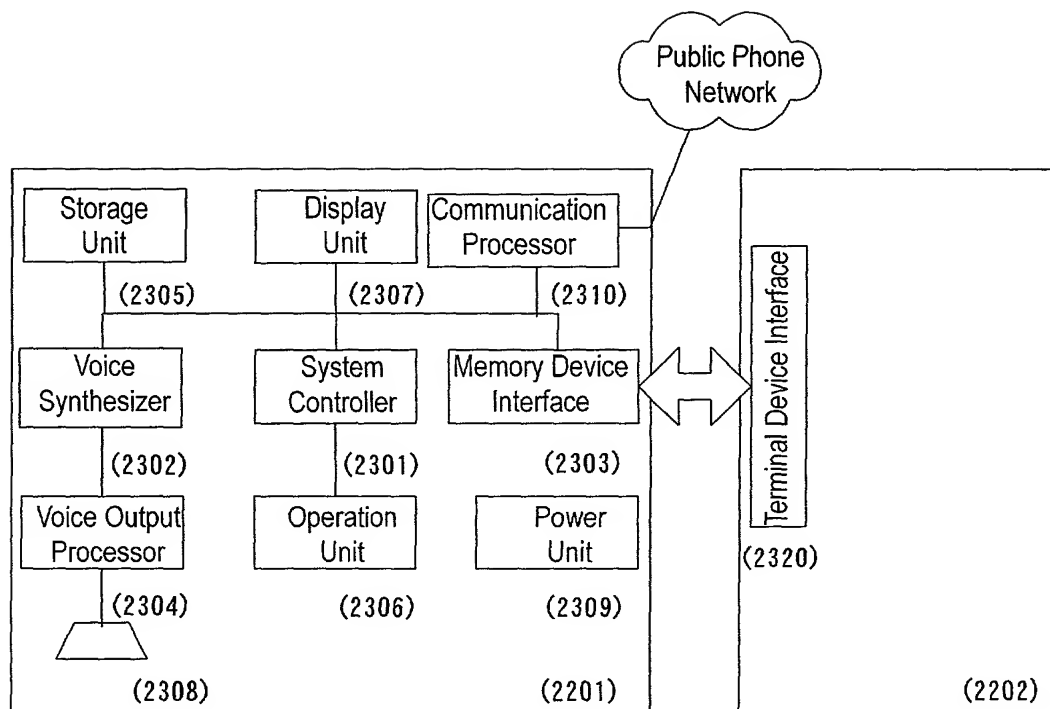


Fig. 24

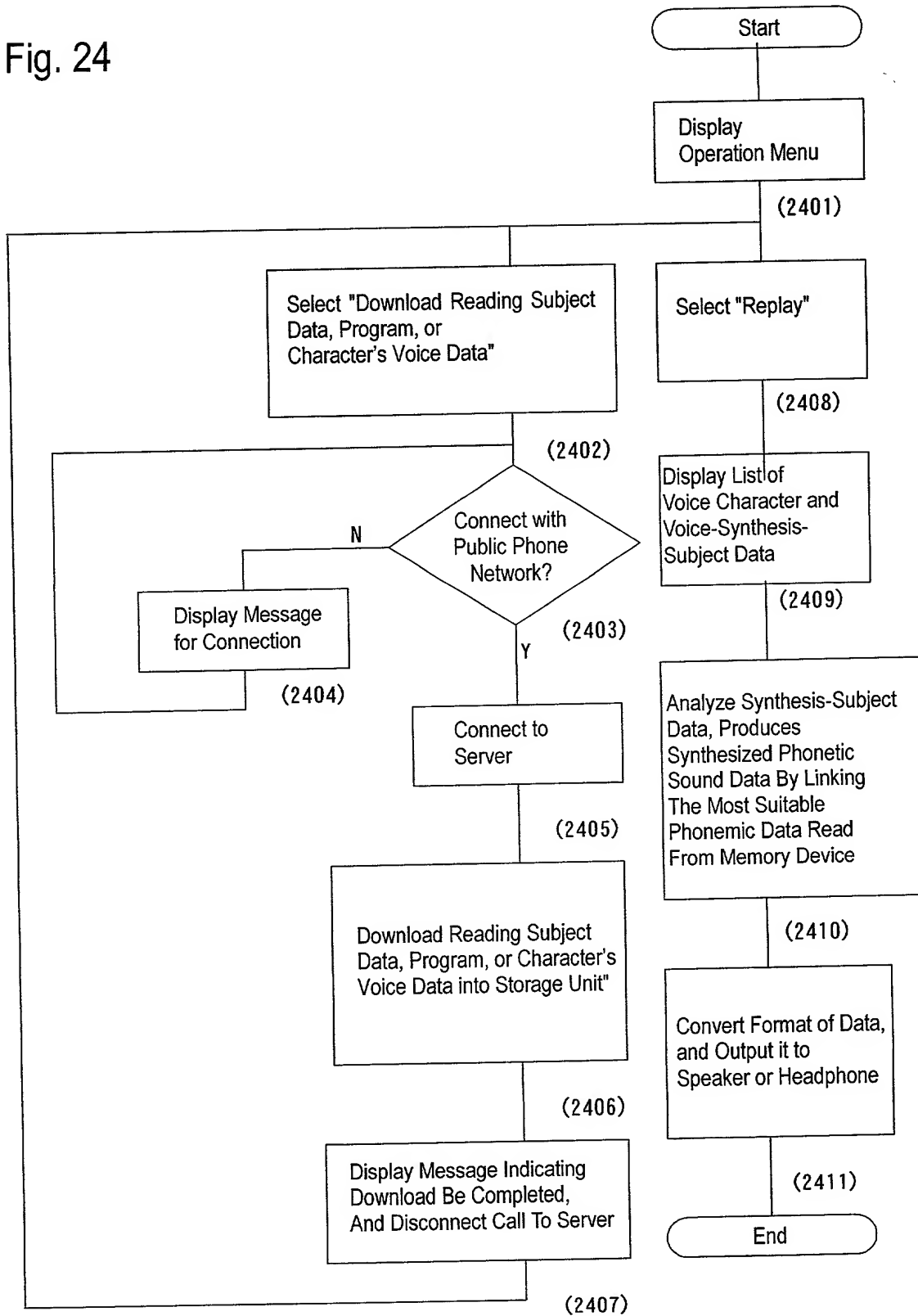


Fig. 25

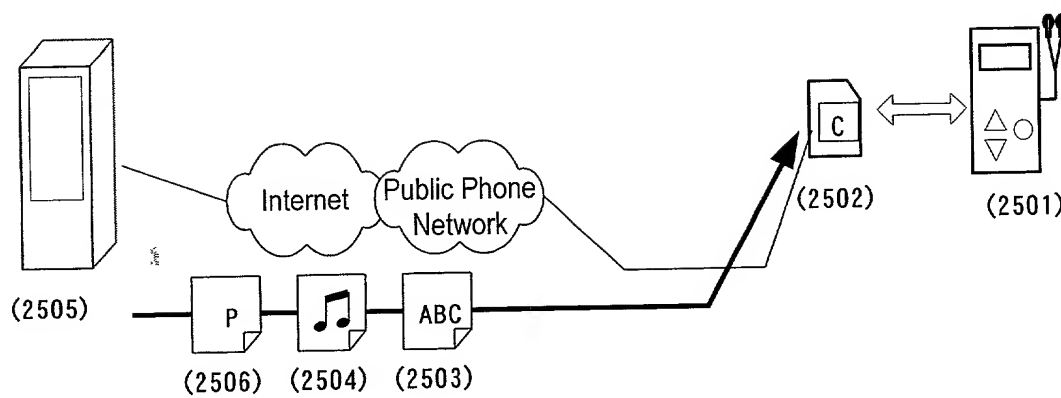


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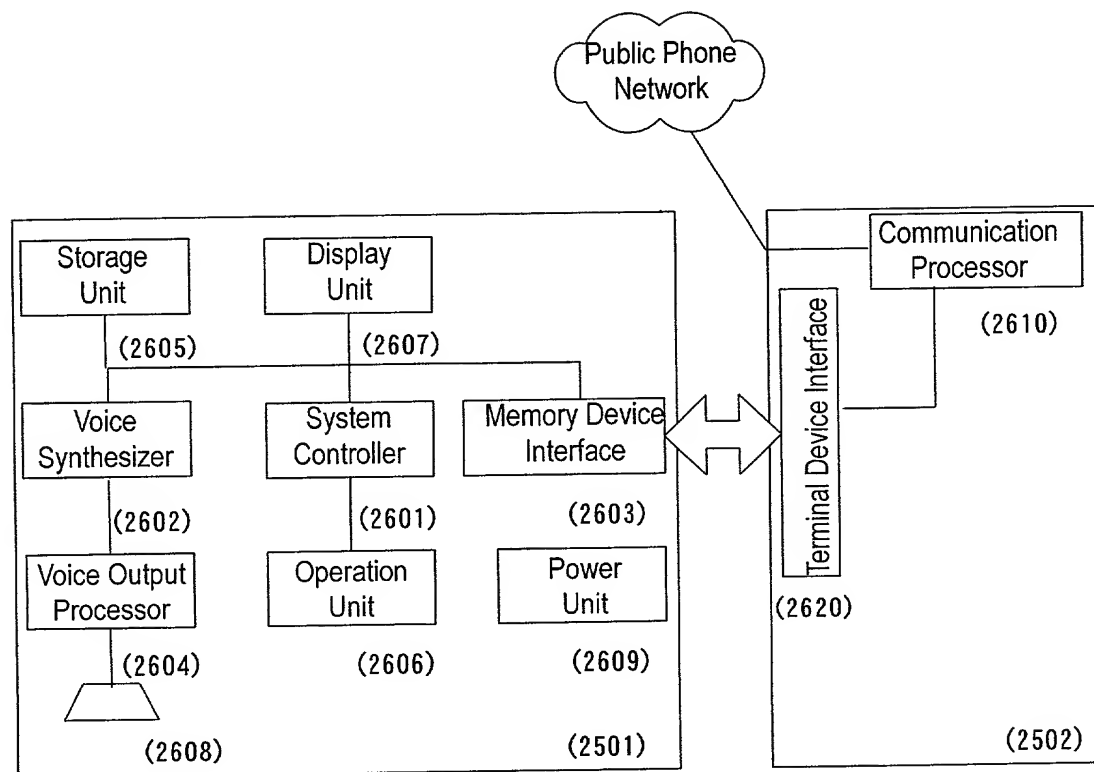


Fig. 27

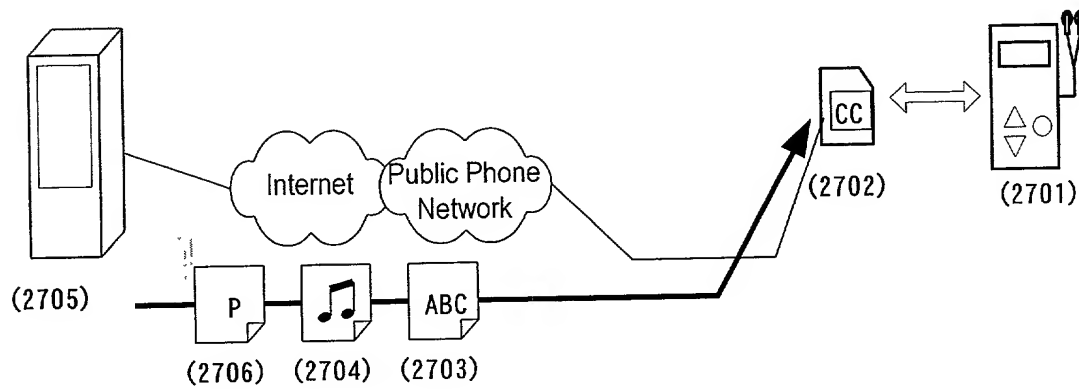


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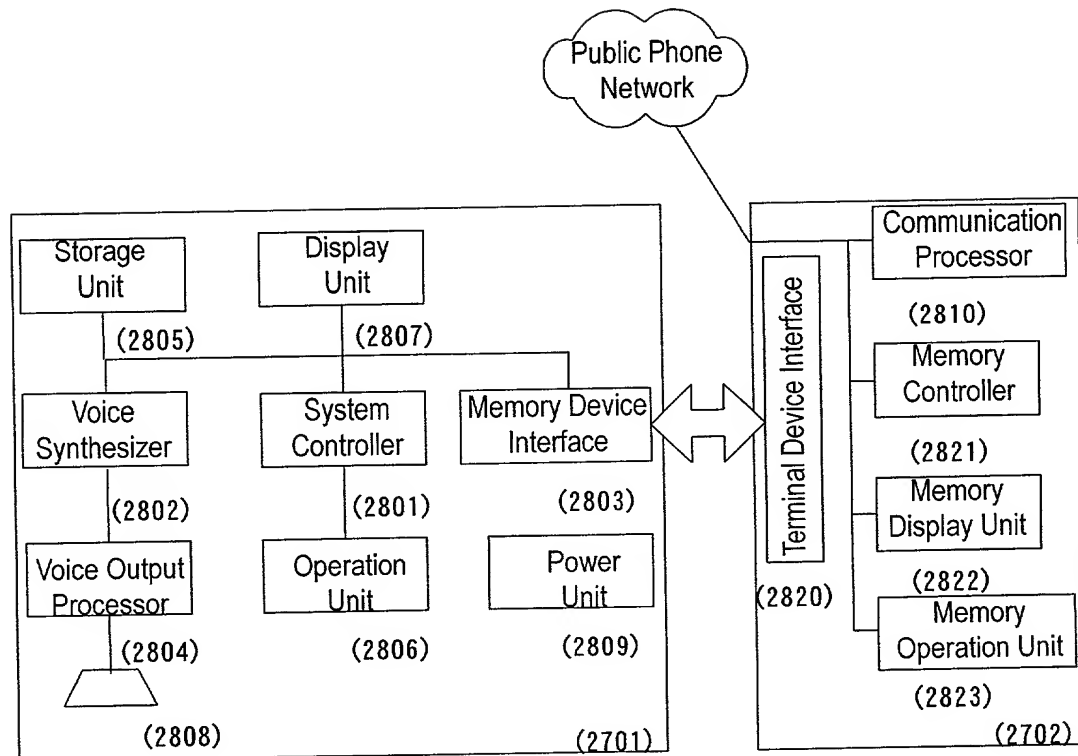


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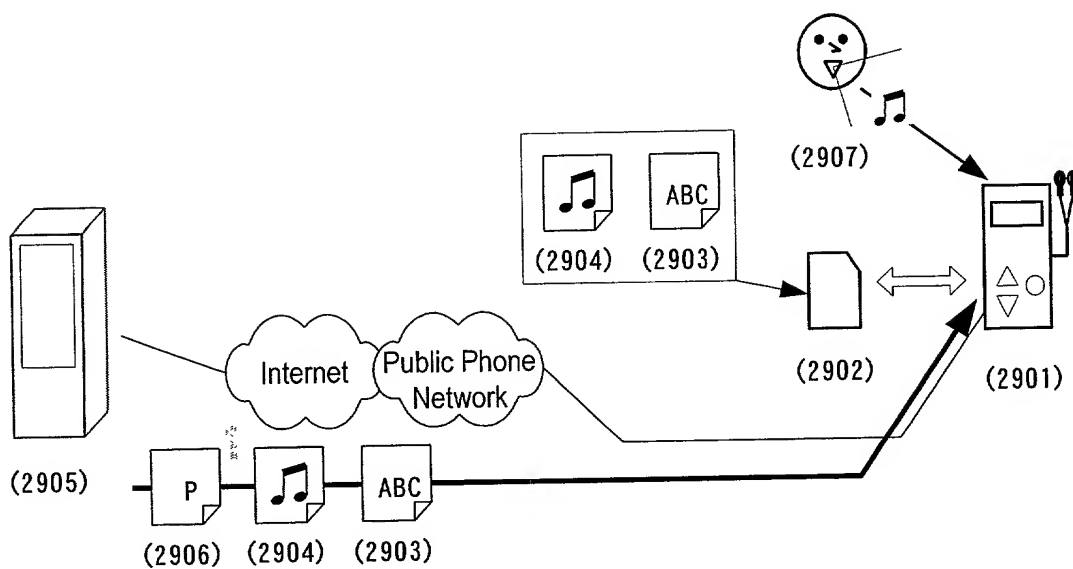


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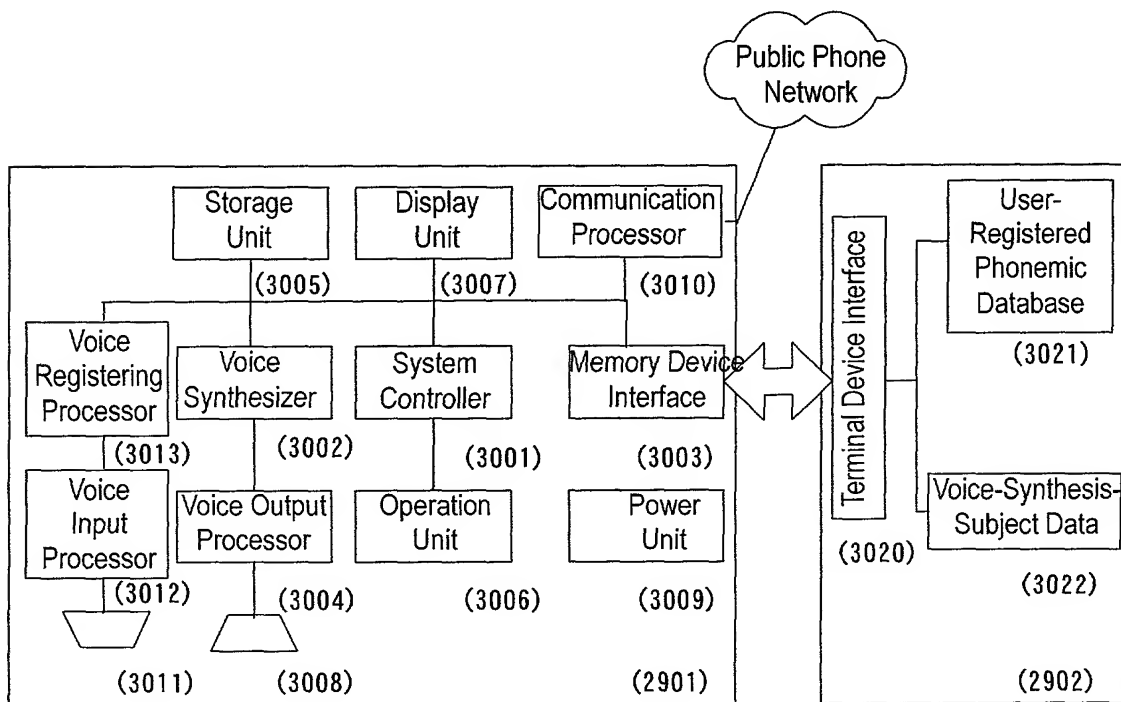


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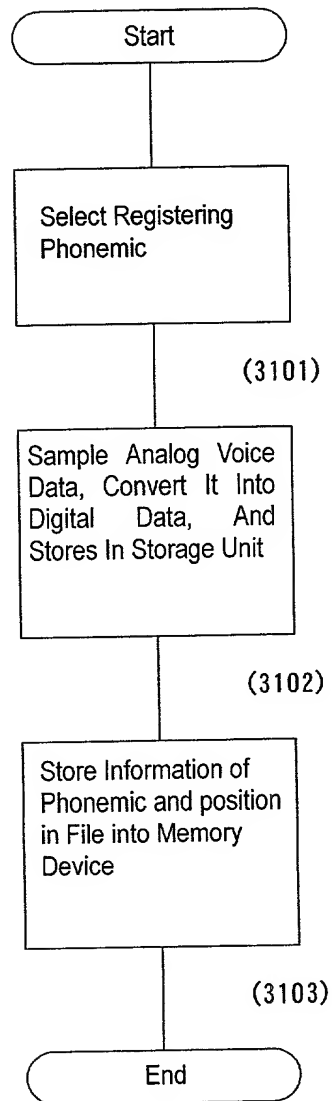


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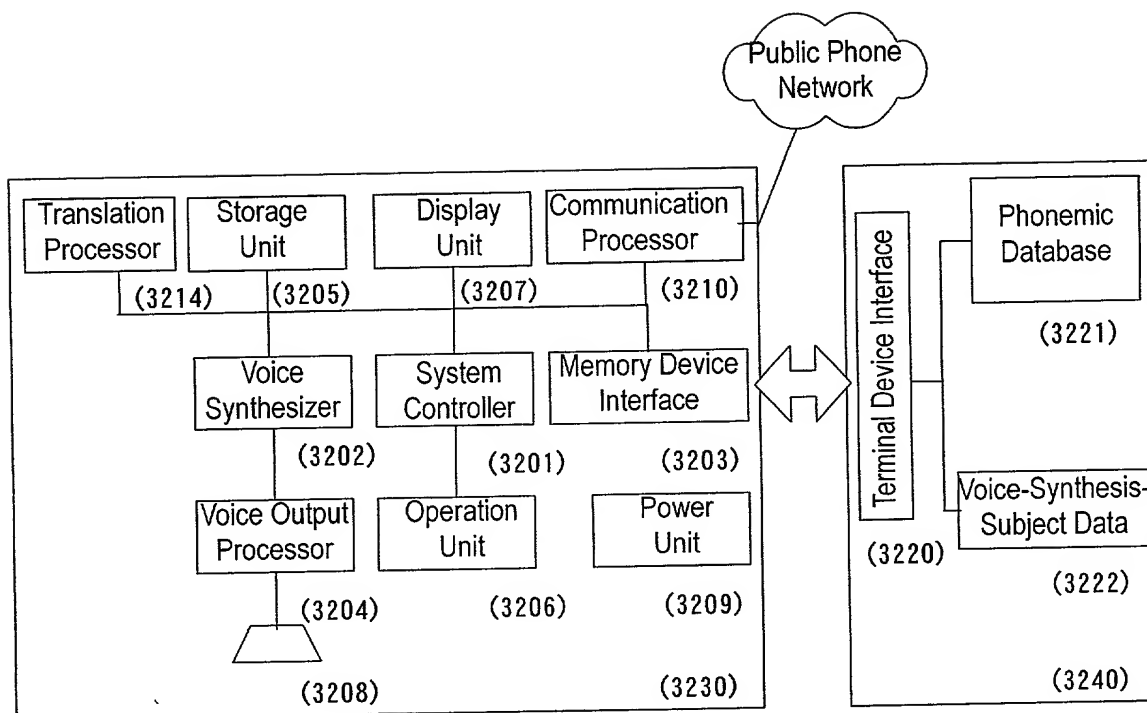


Fig. 33

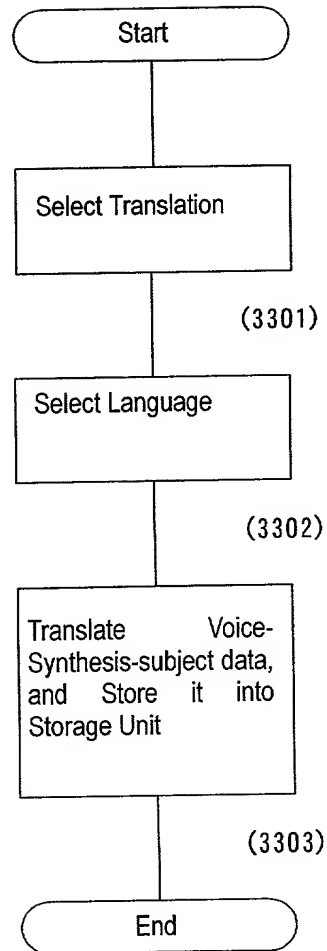


Fig. 34

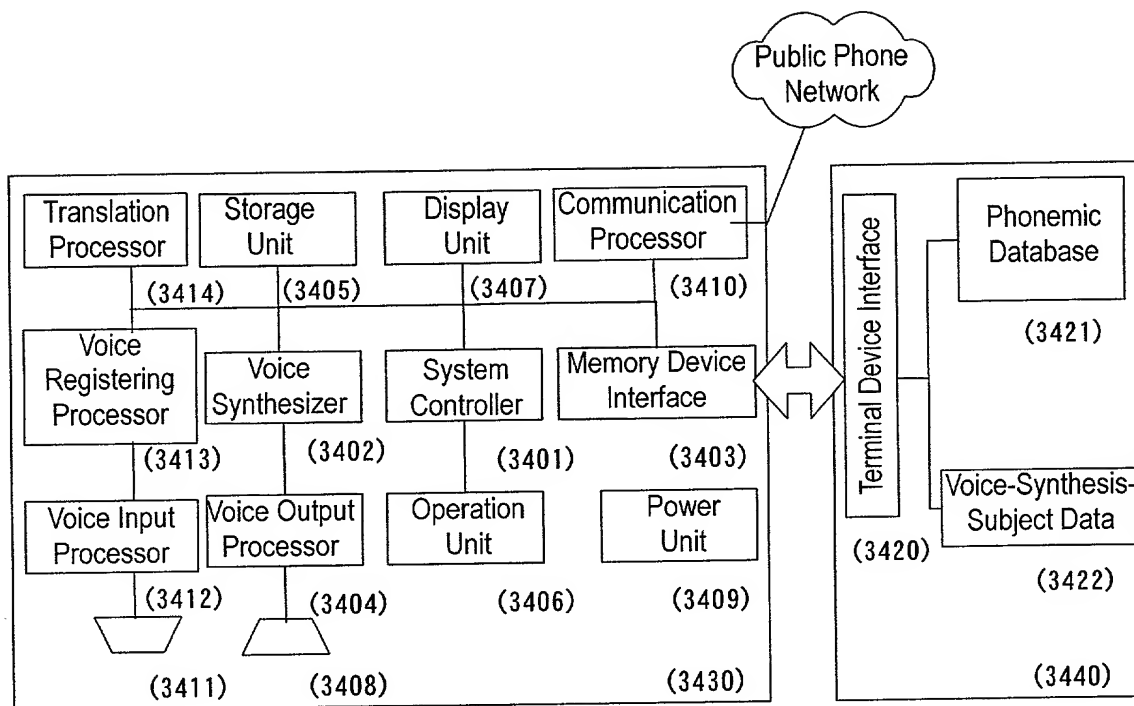


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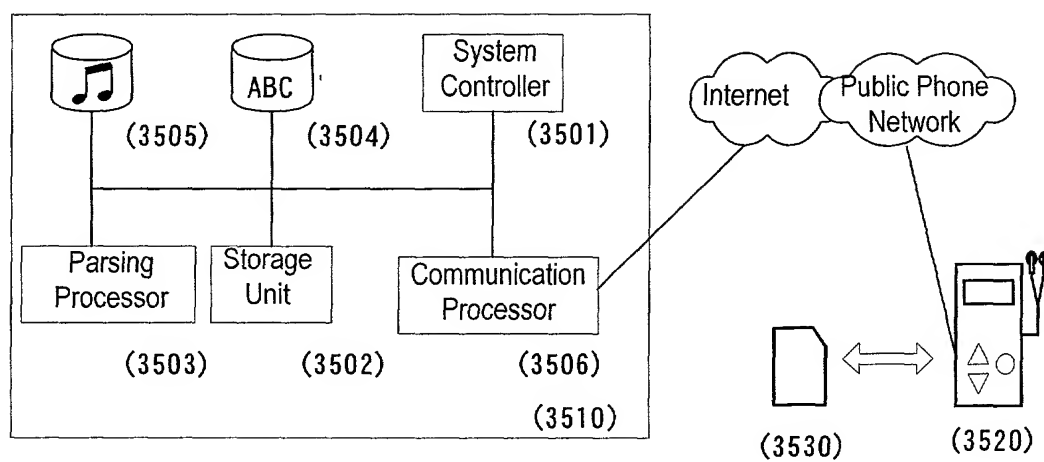


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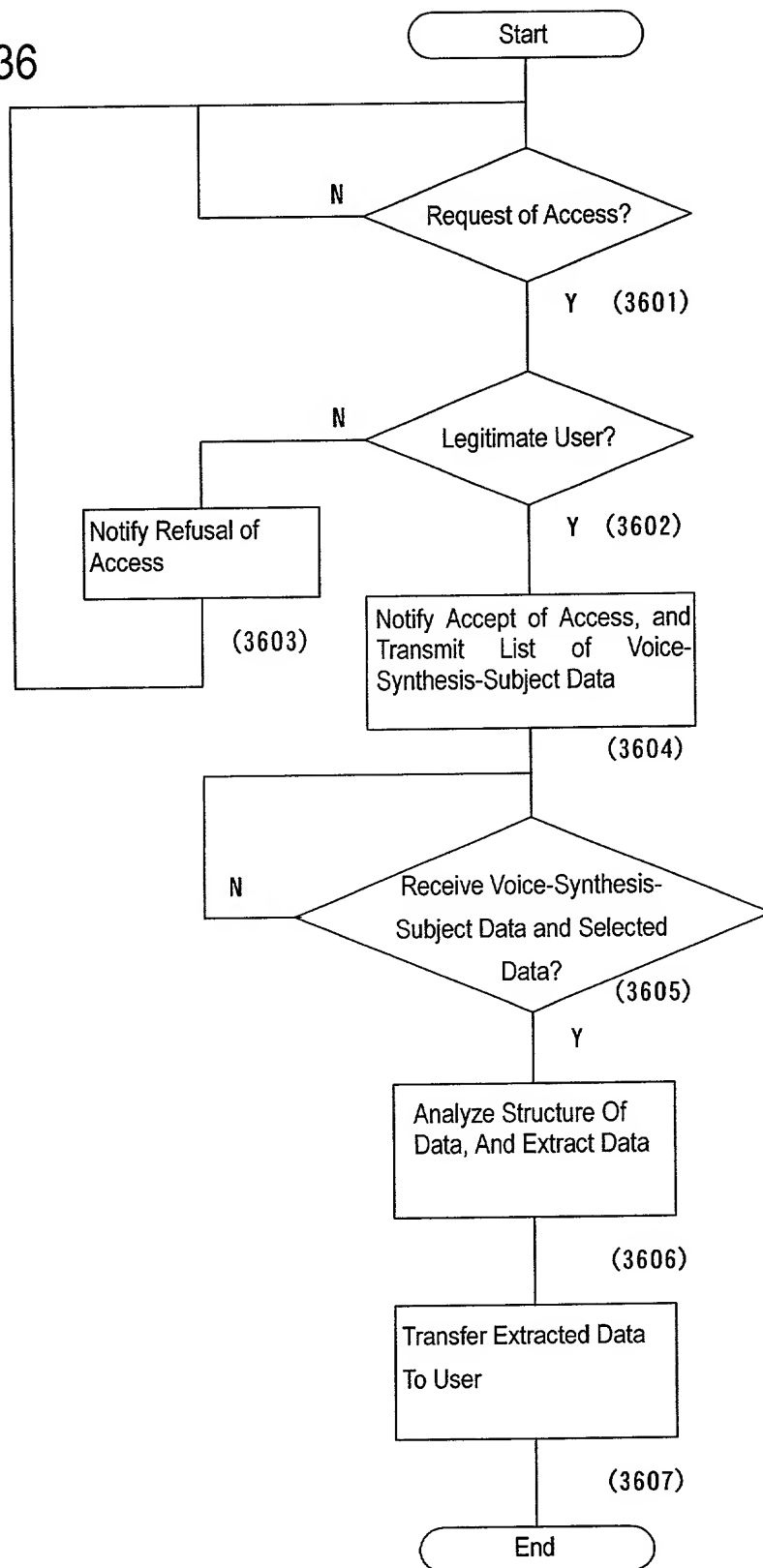


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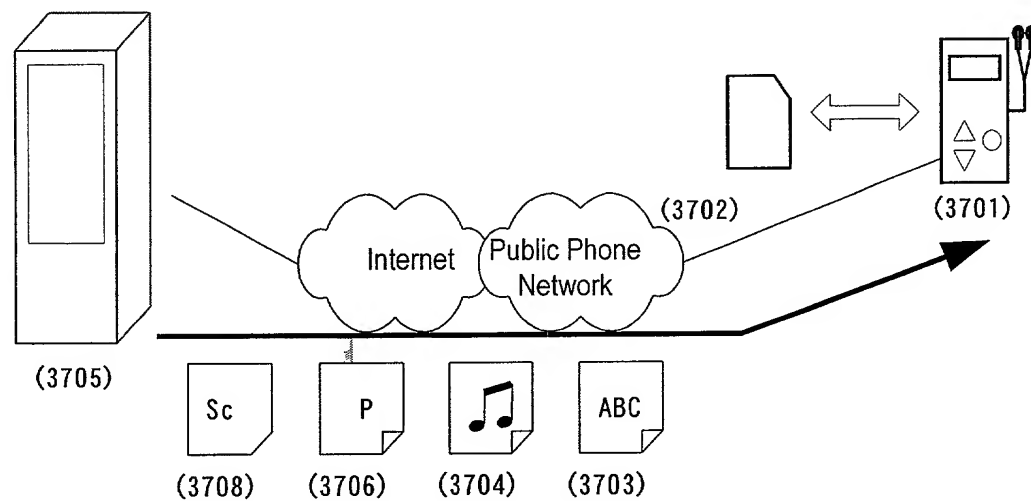


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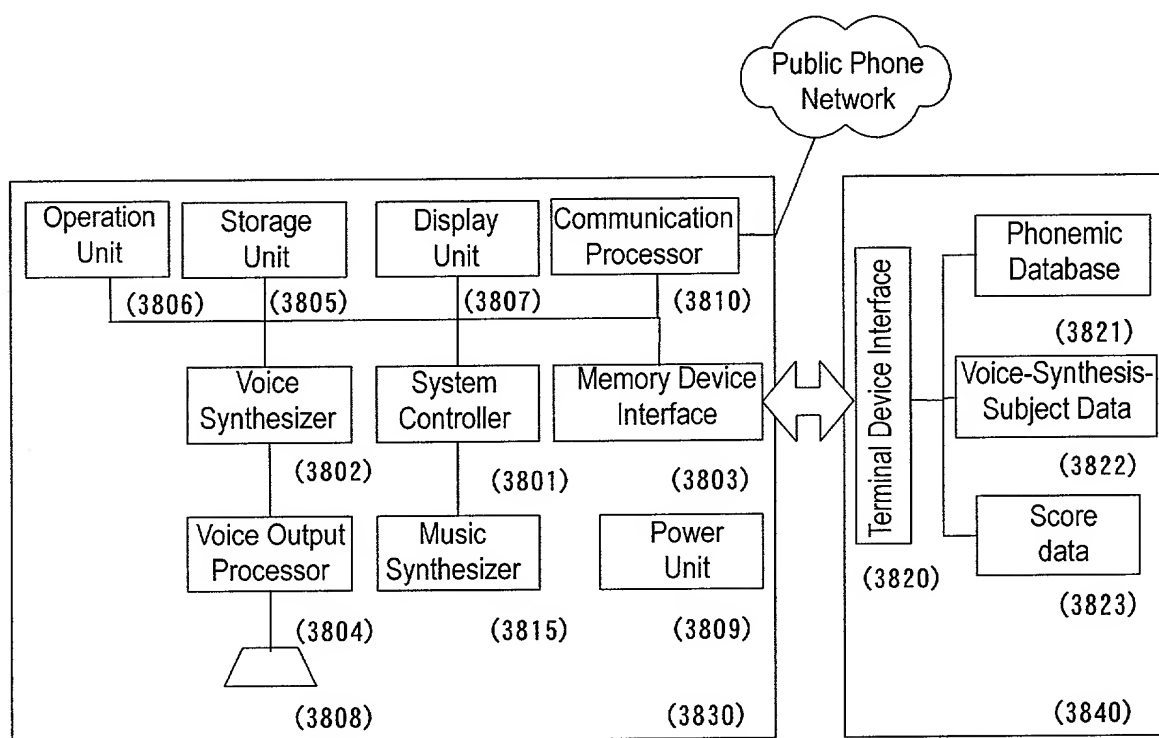


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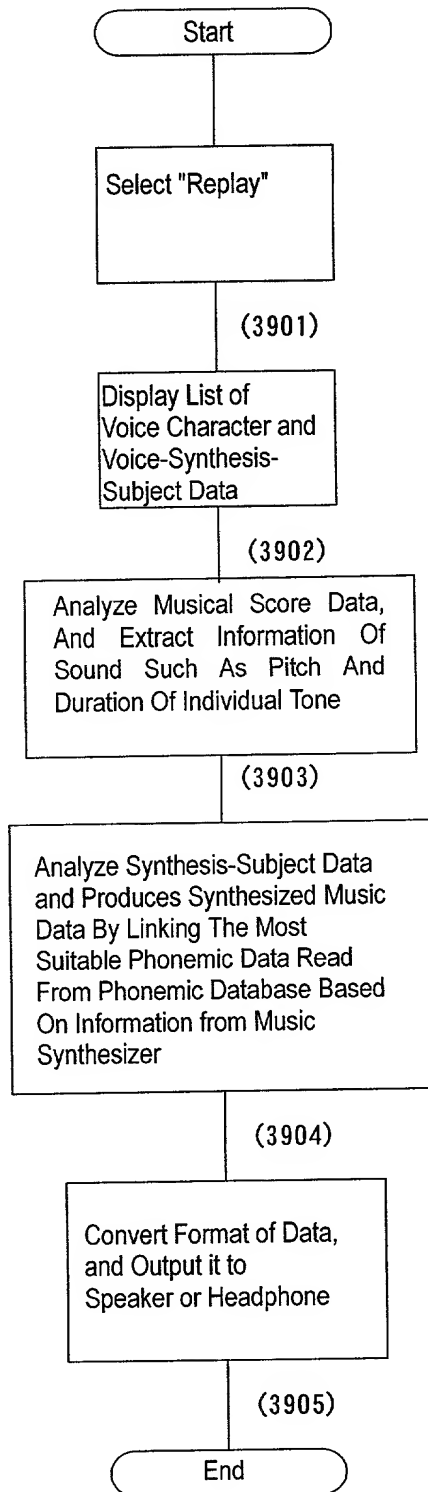


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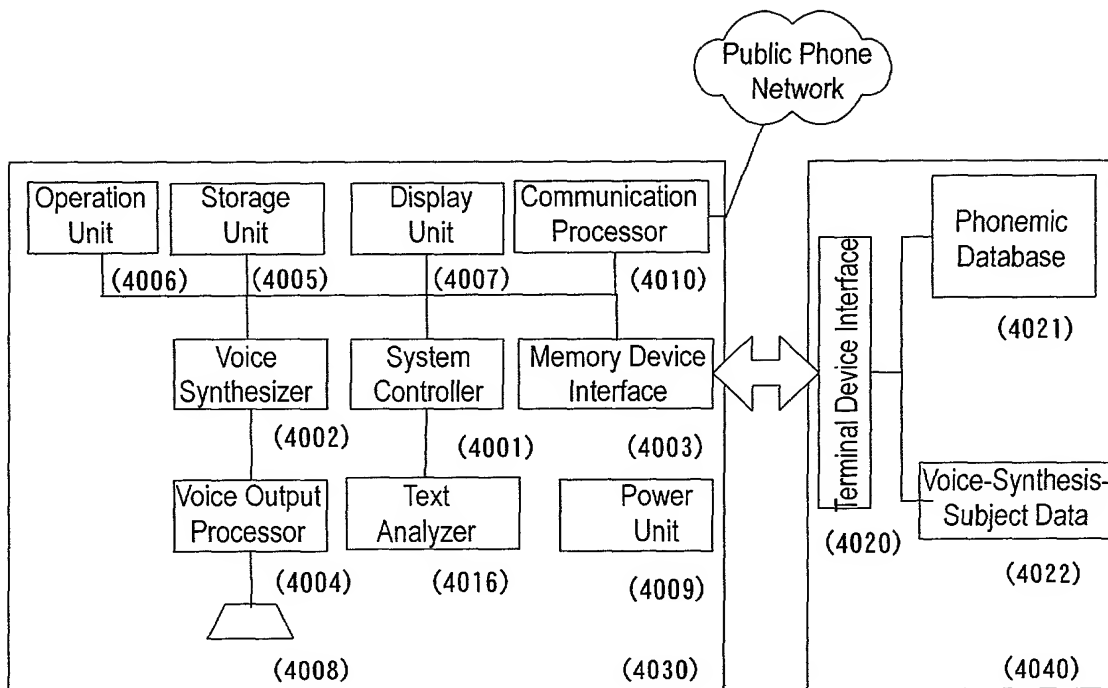


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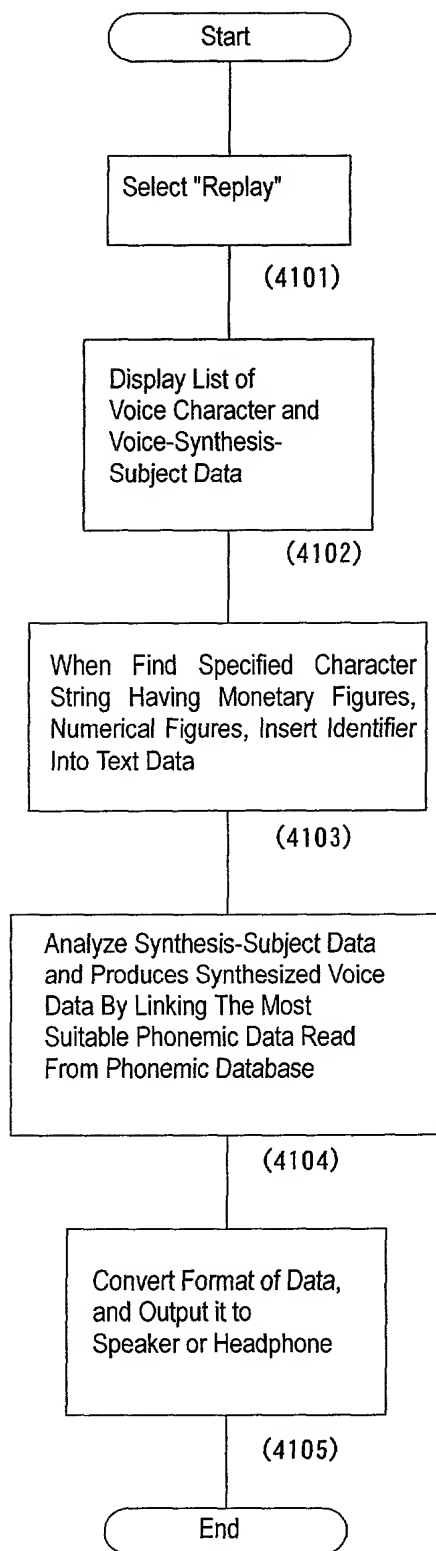


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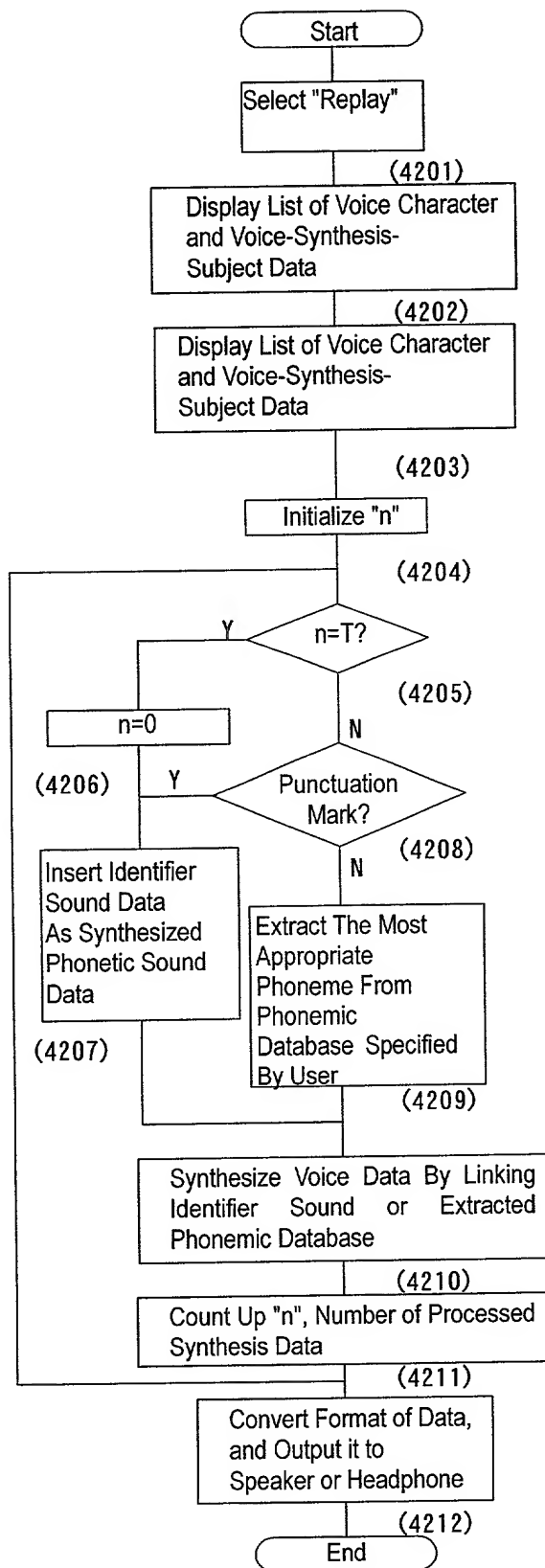


Fig. 43

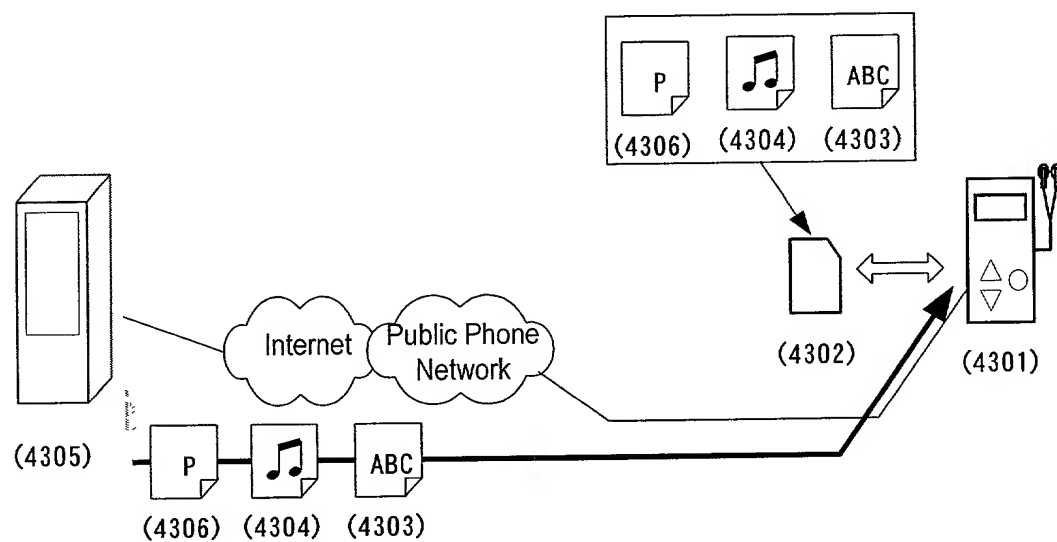


Fig. 44

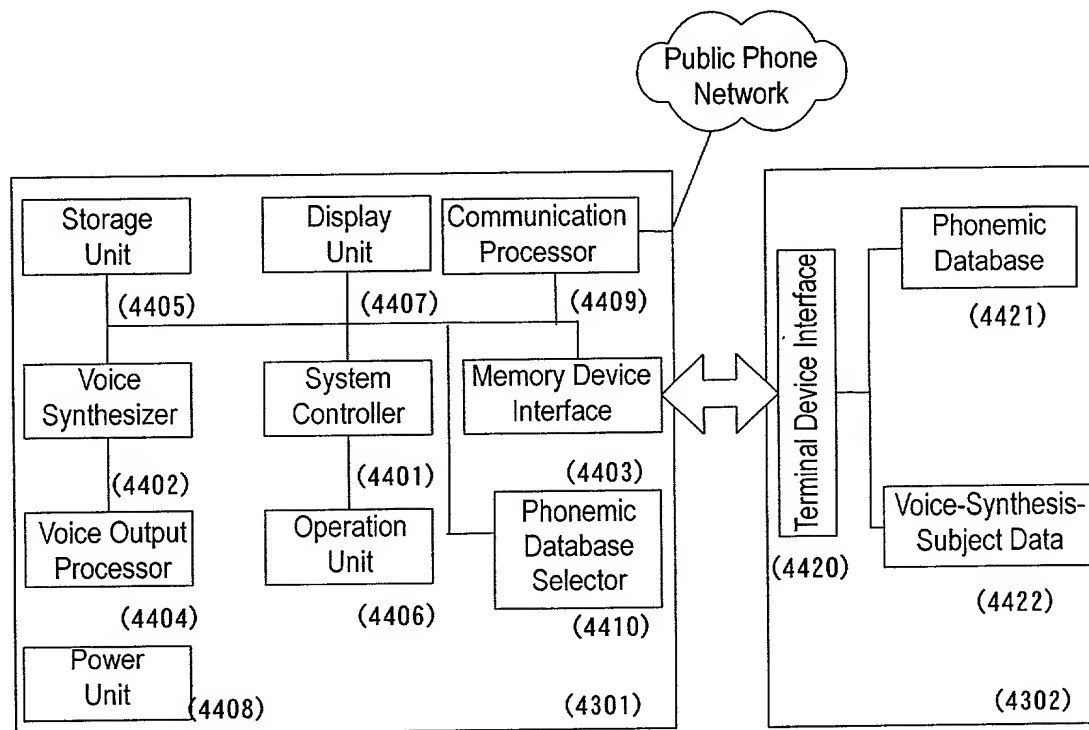


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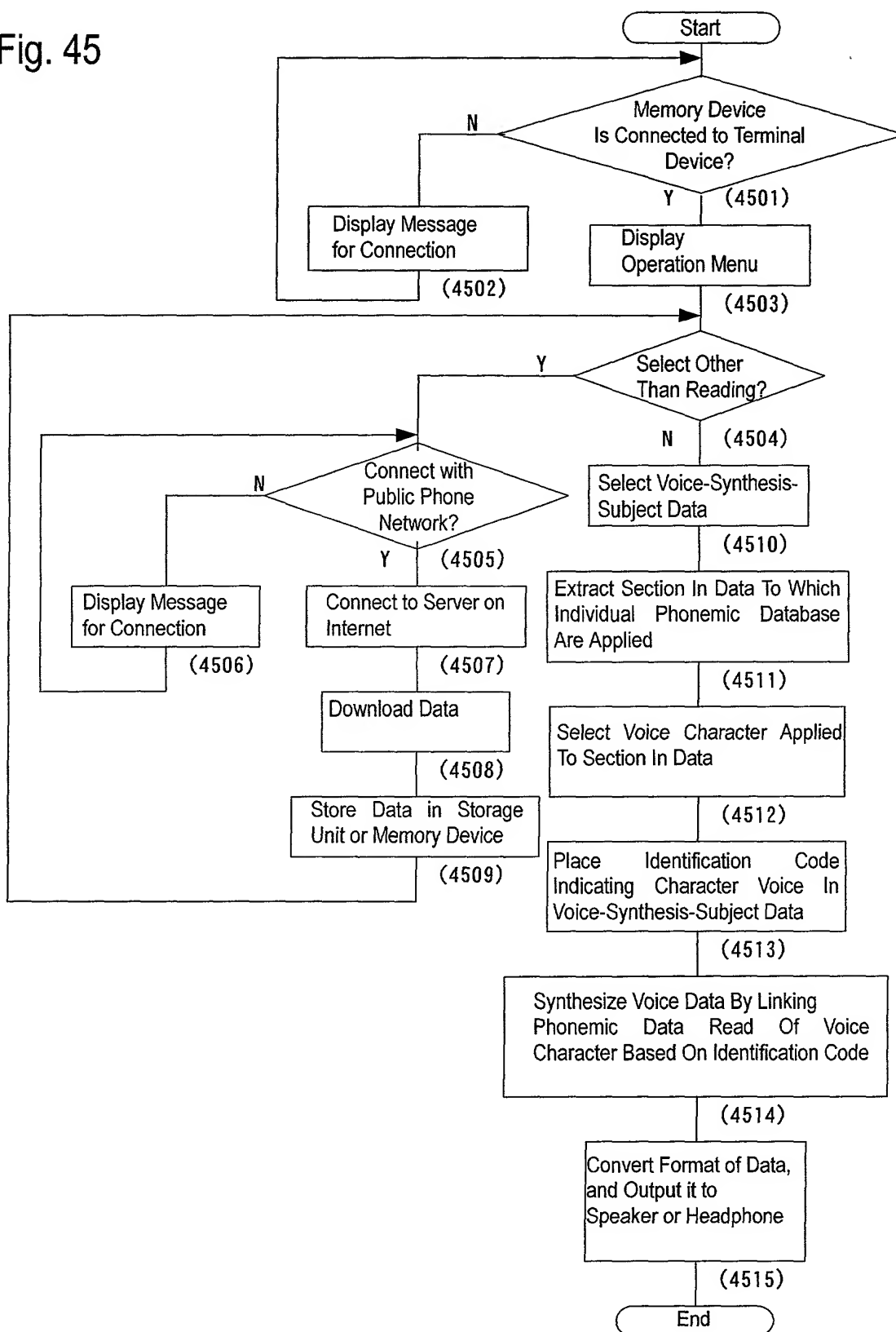


Fig. 46

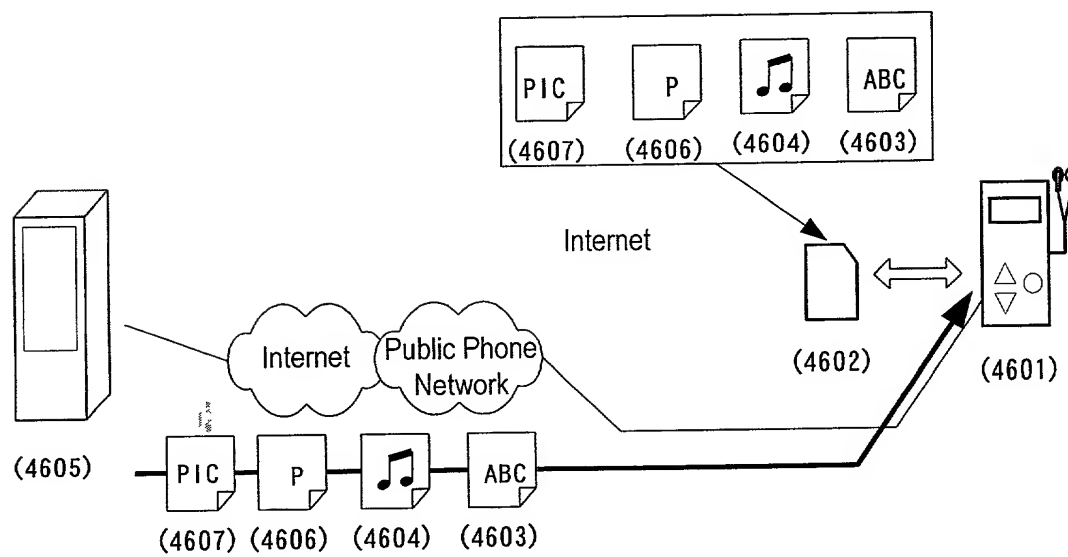


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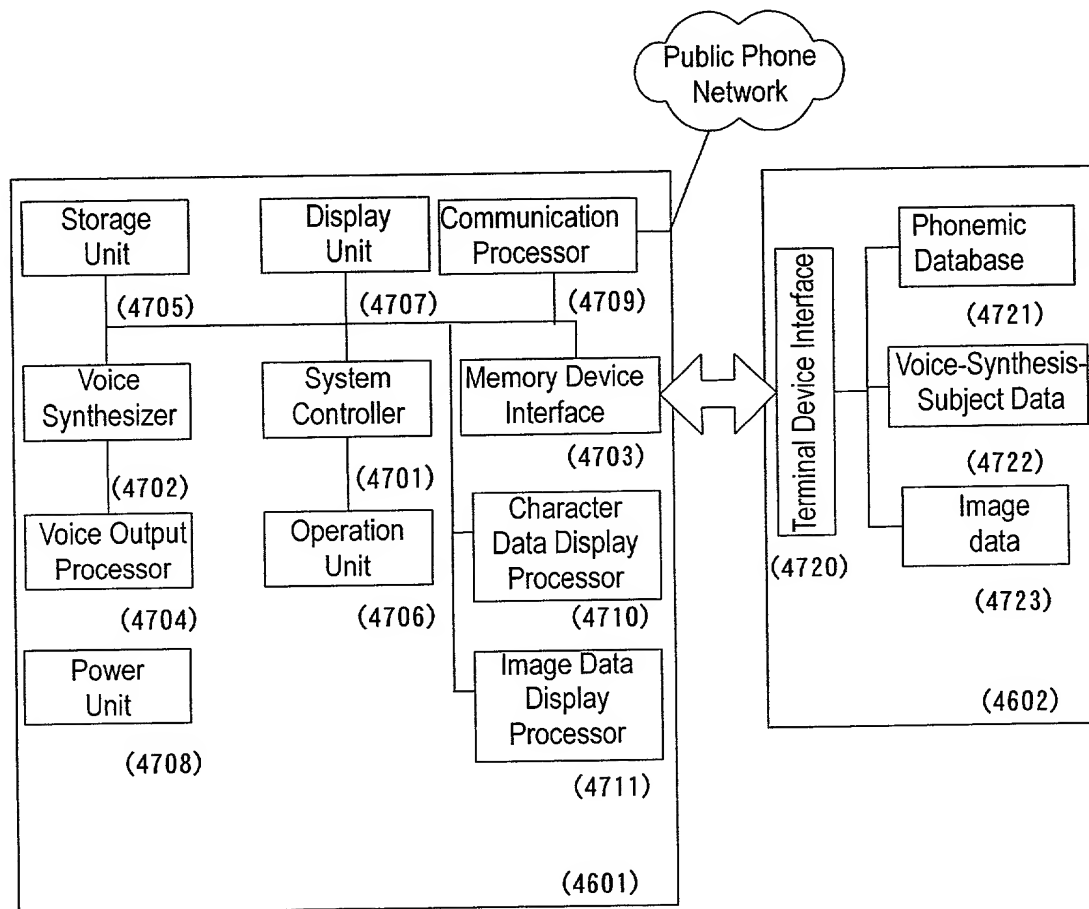


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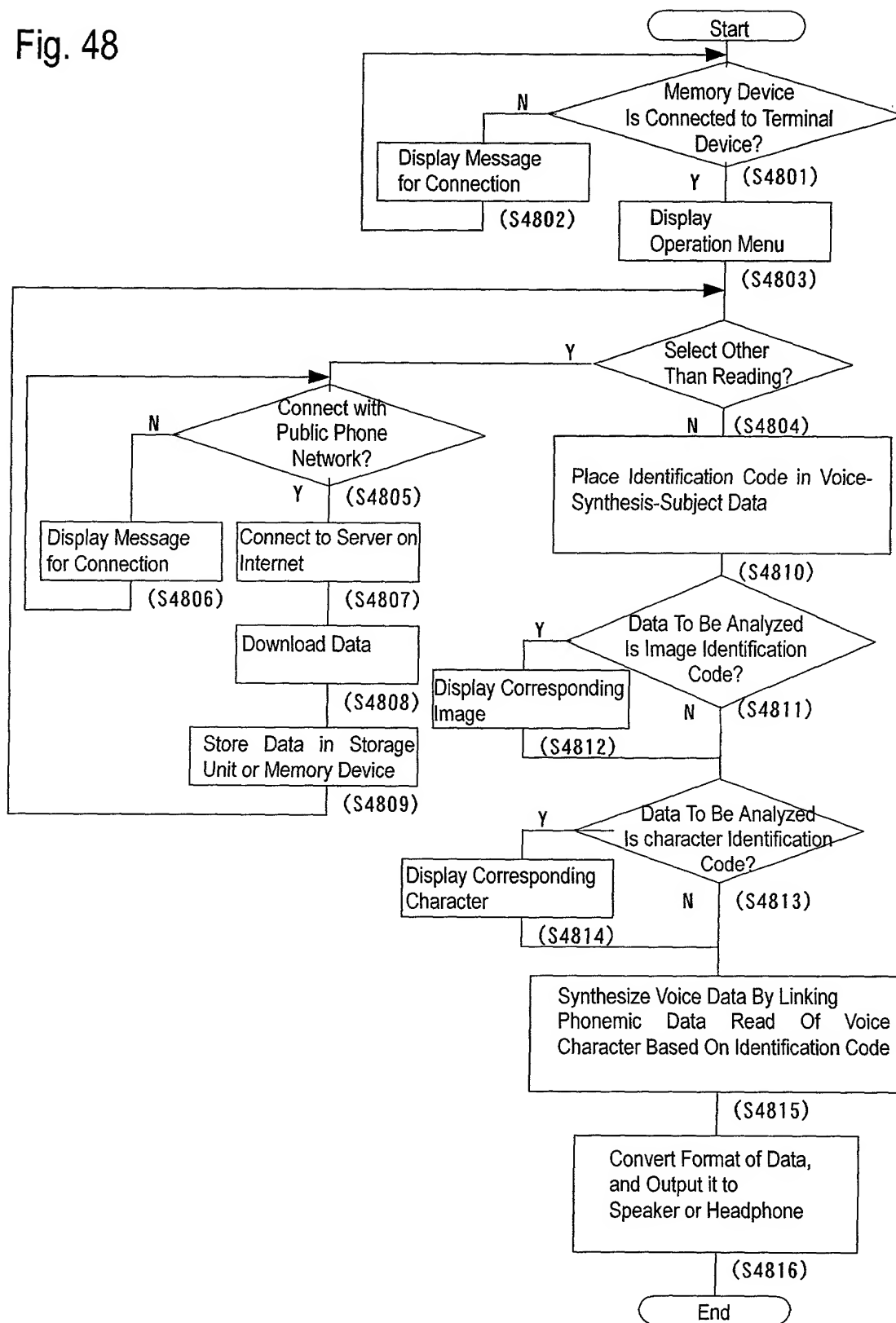


Fig. 49

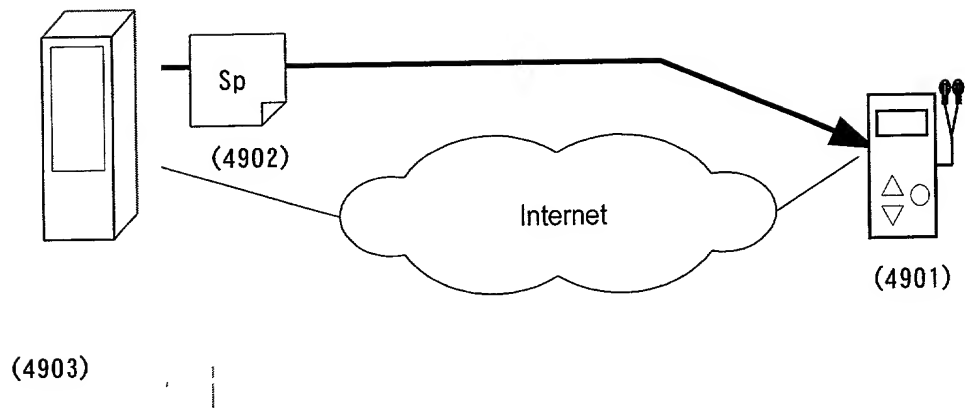


Fig. 50

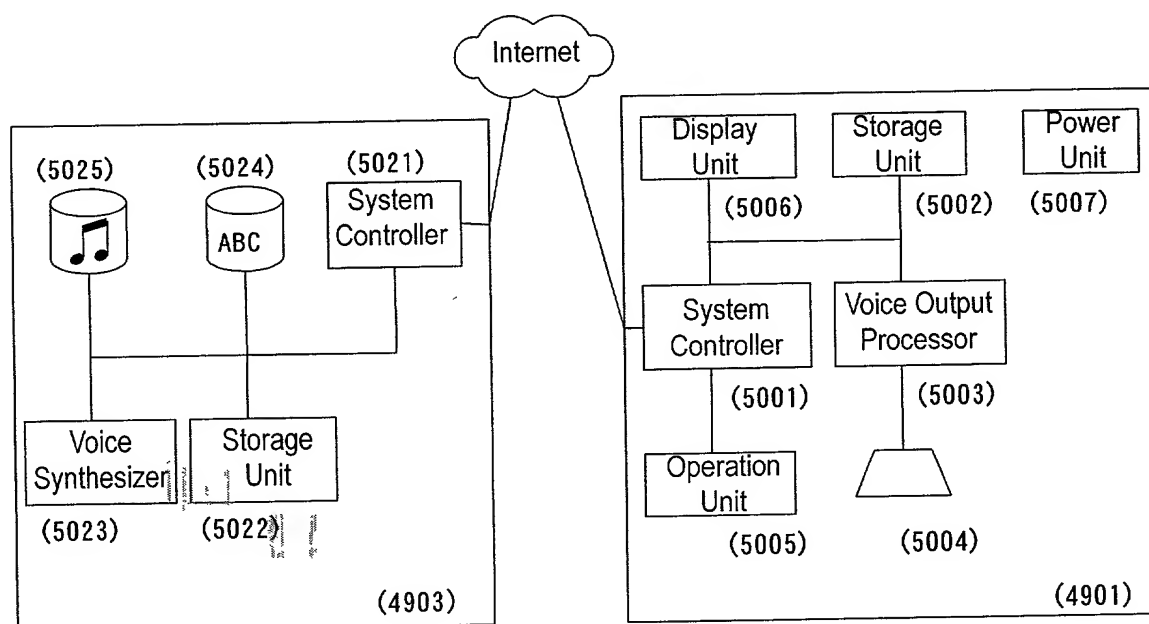


Fig. 51

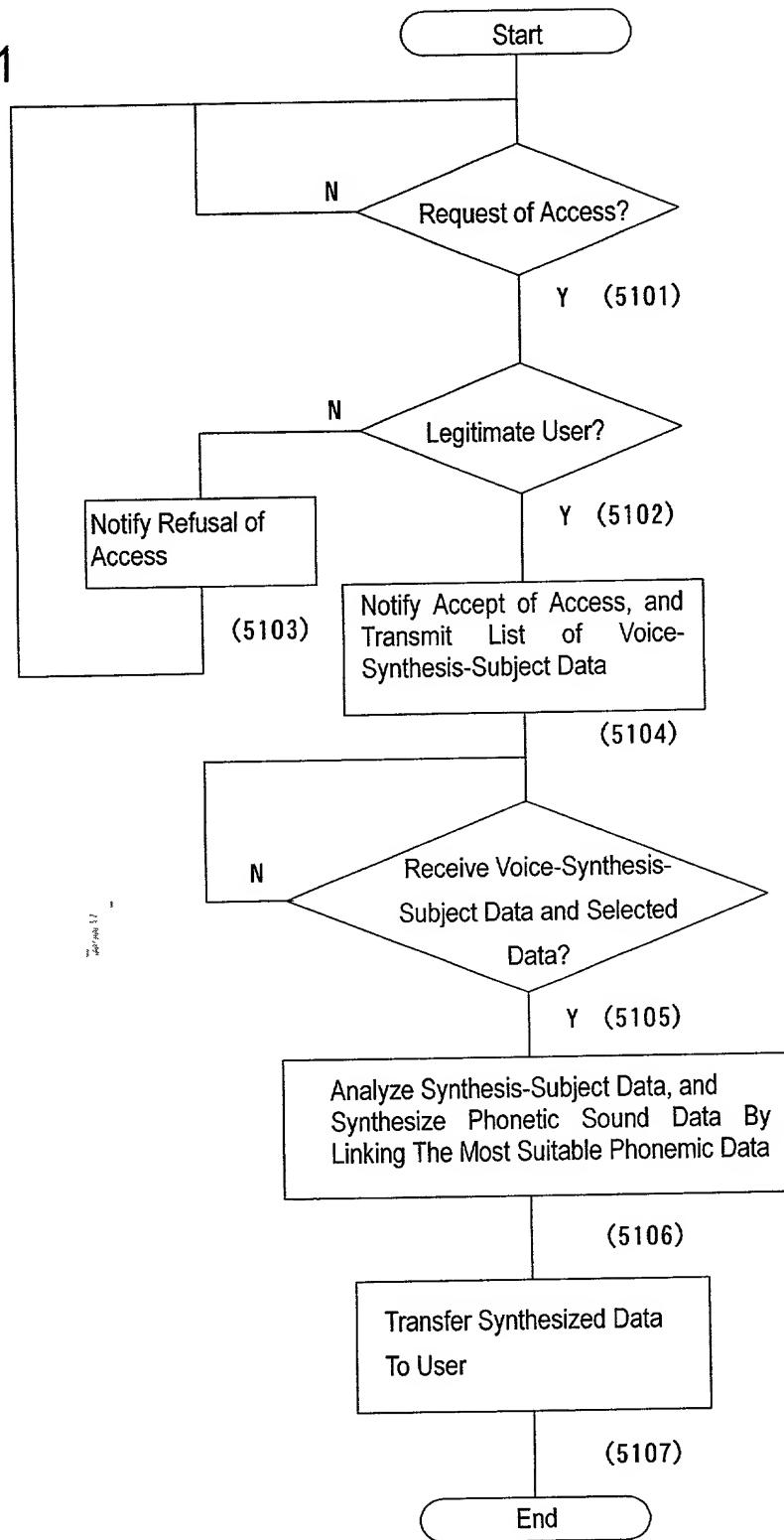


Fig. 52

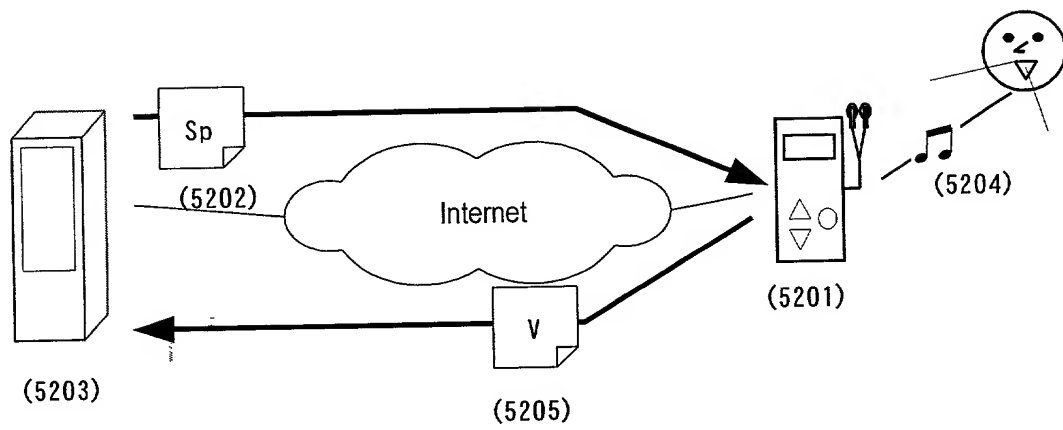


Fig. 53

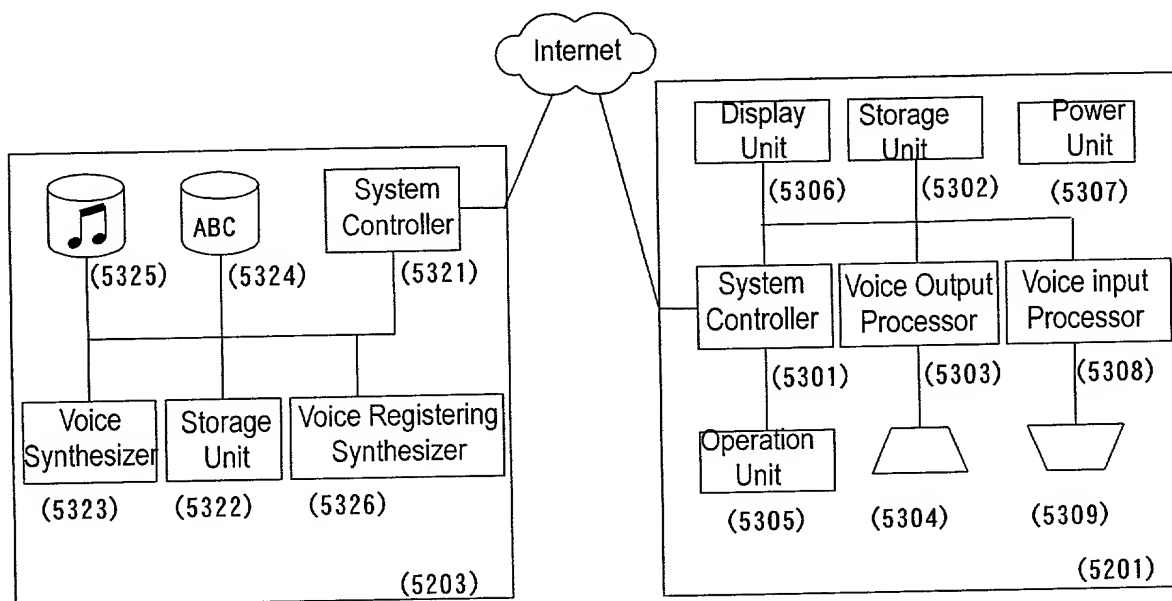


Fig. 54

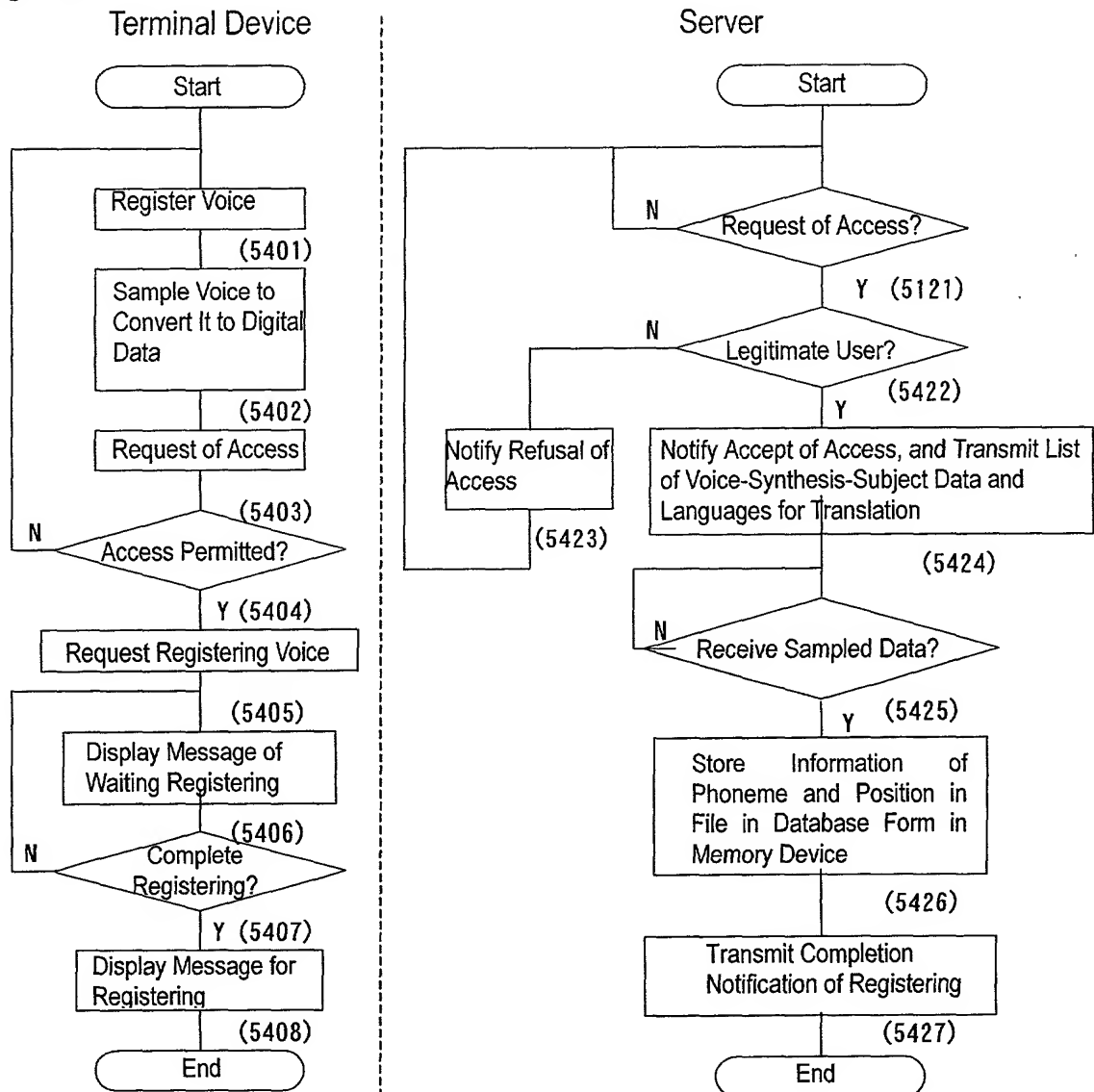
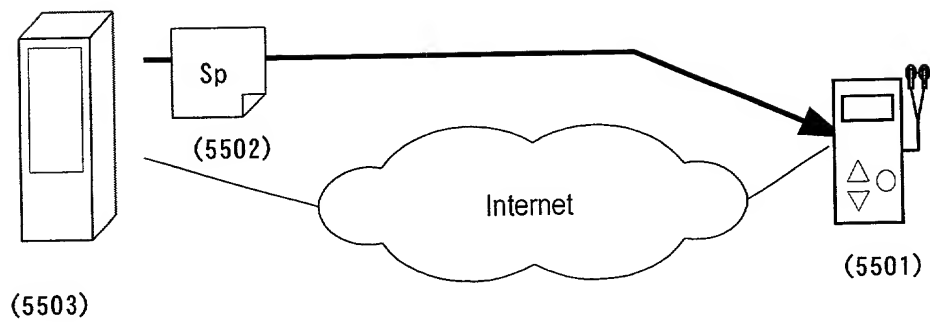


Fig. 55



1 21
2 22

Fig. 56

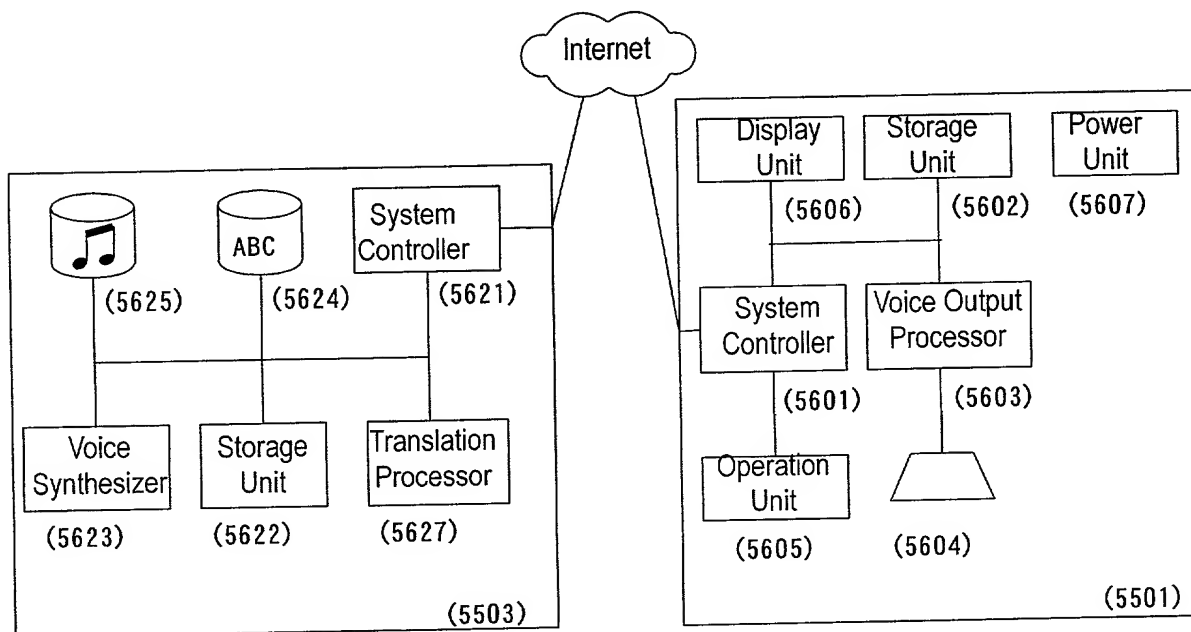


Fig. 57

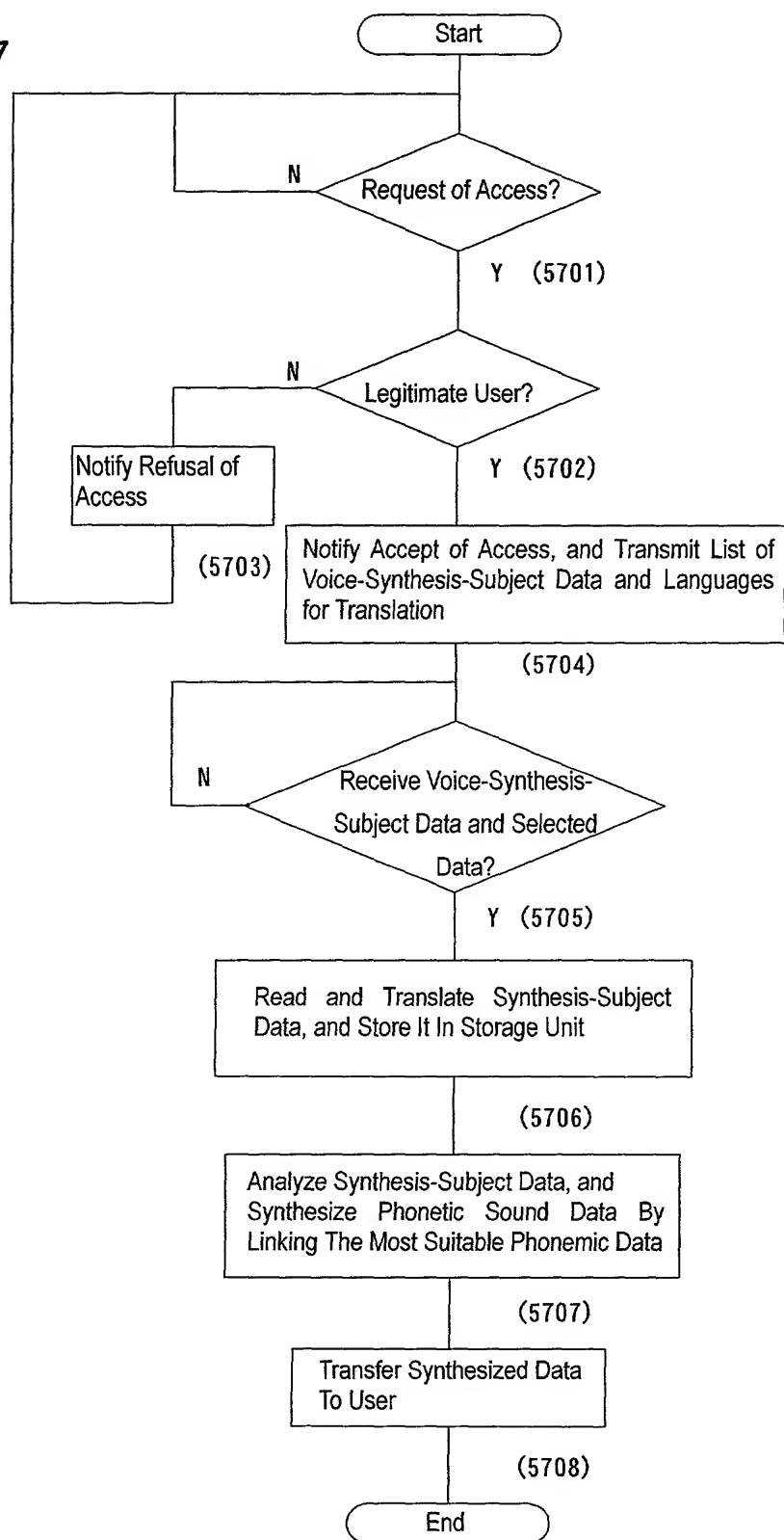


Fig. 58

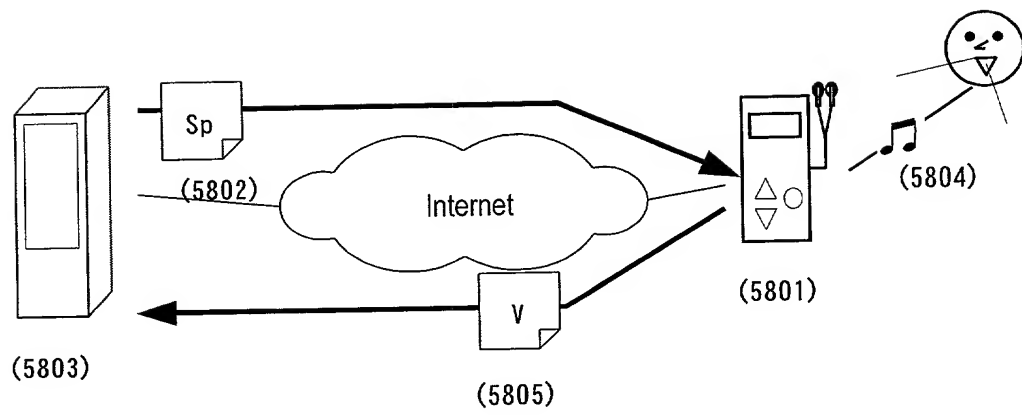


Fig. 59

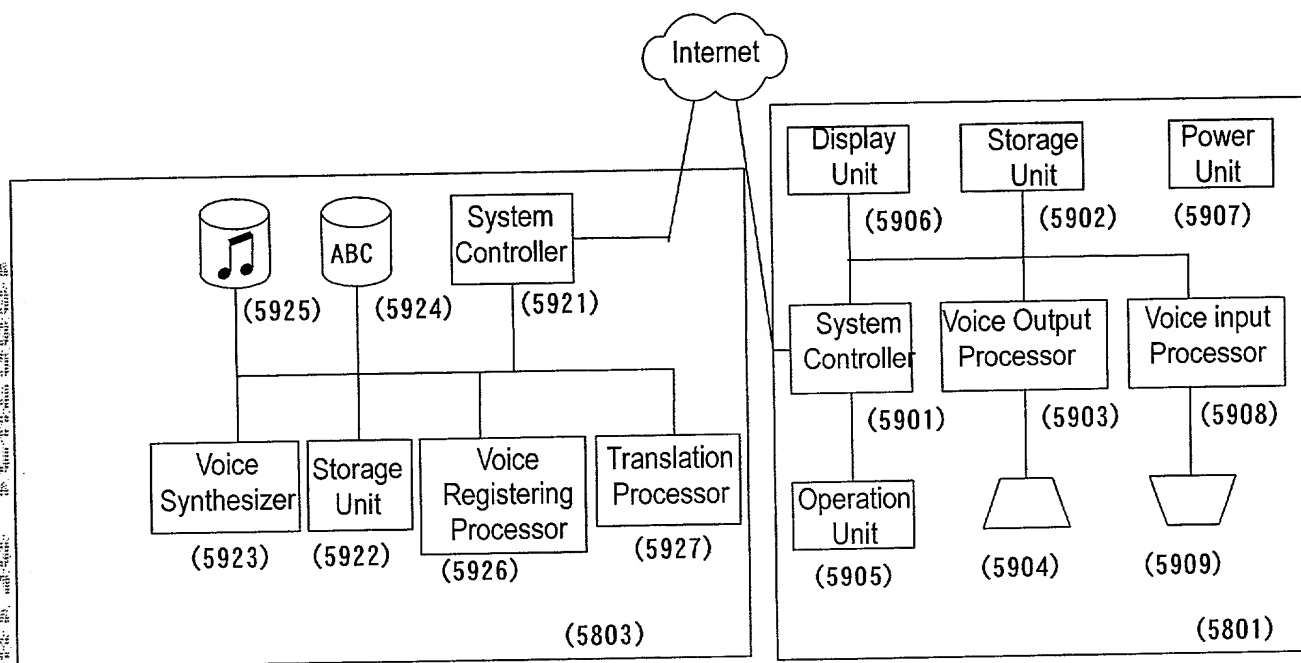


Fig. 60

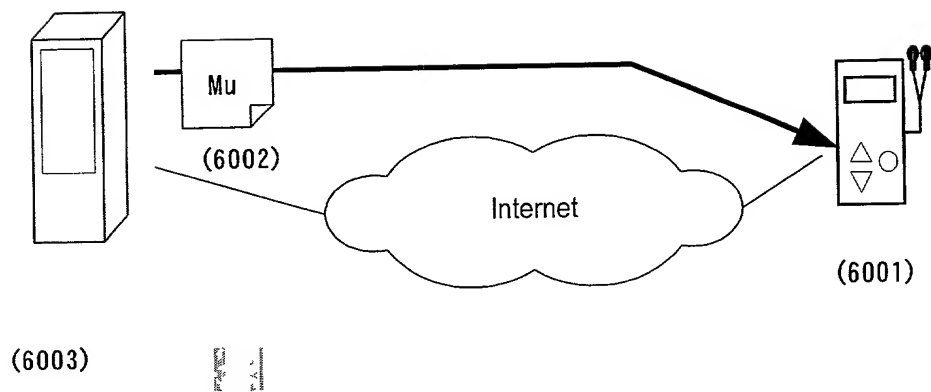


Fig. 61

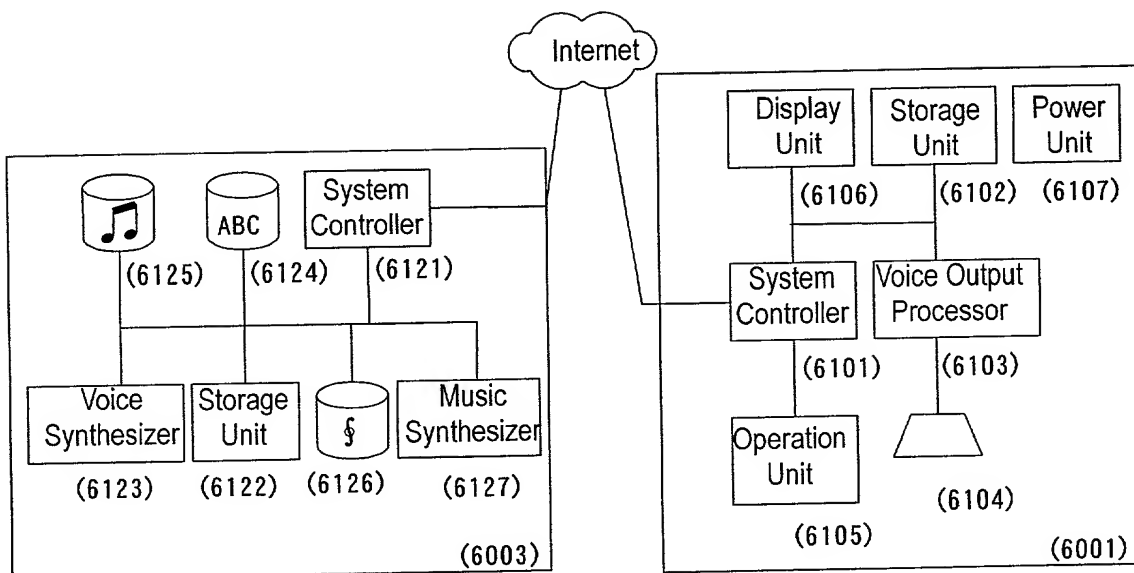


Fig. 62

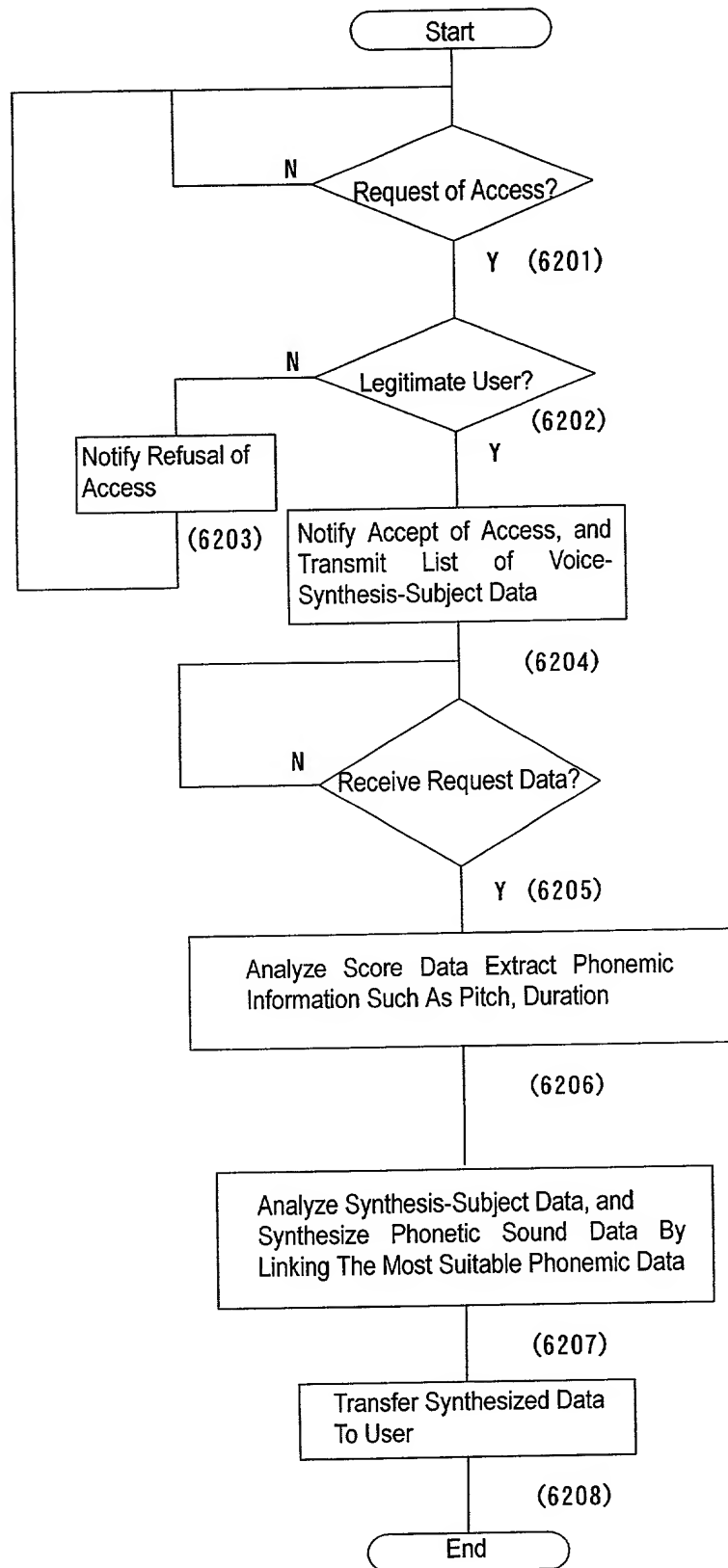


Fig. 63

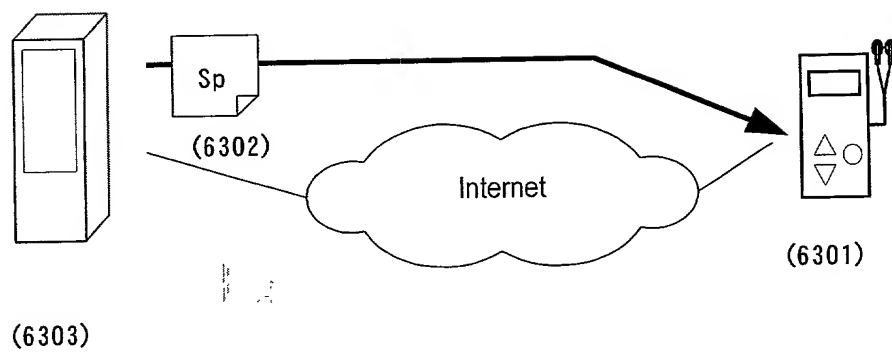


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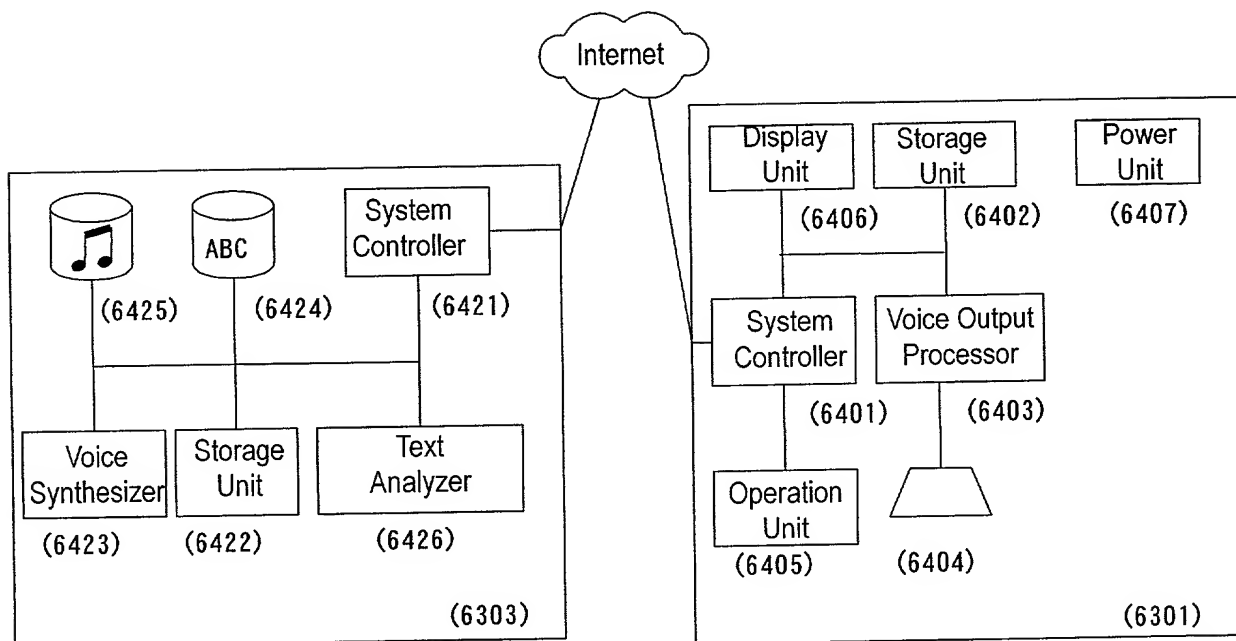


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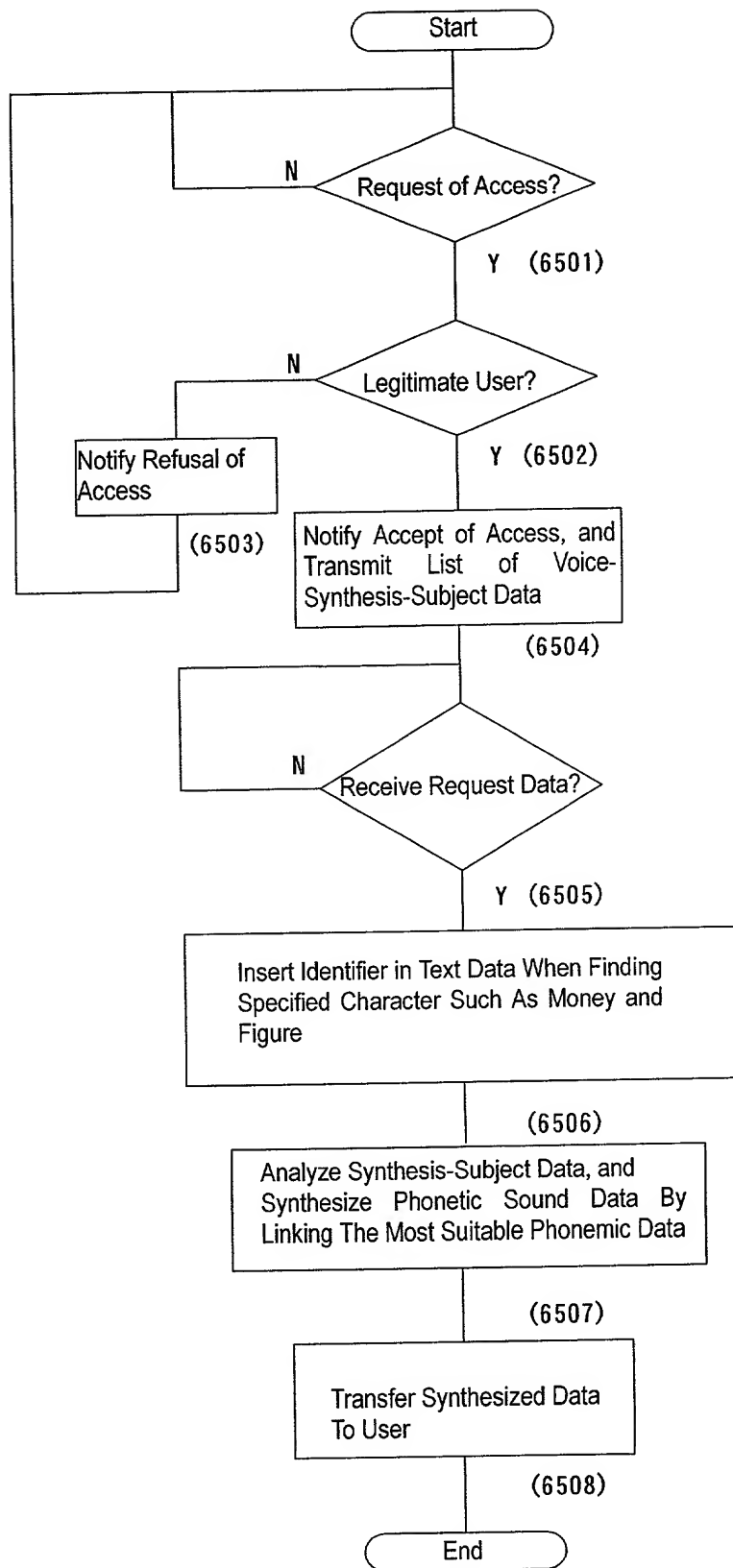


Fig. 66

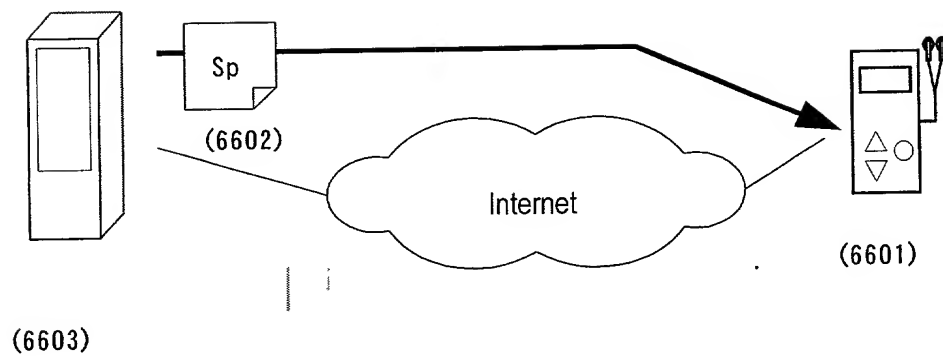


Fig. 67

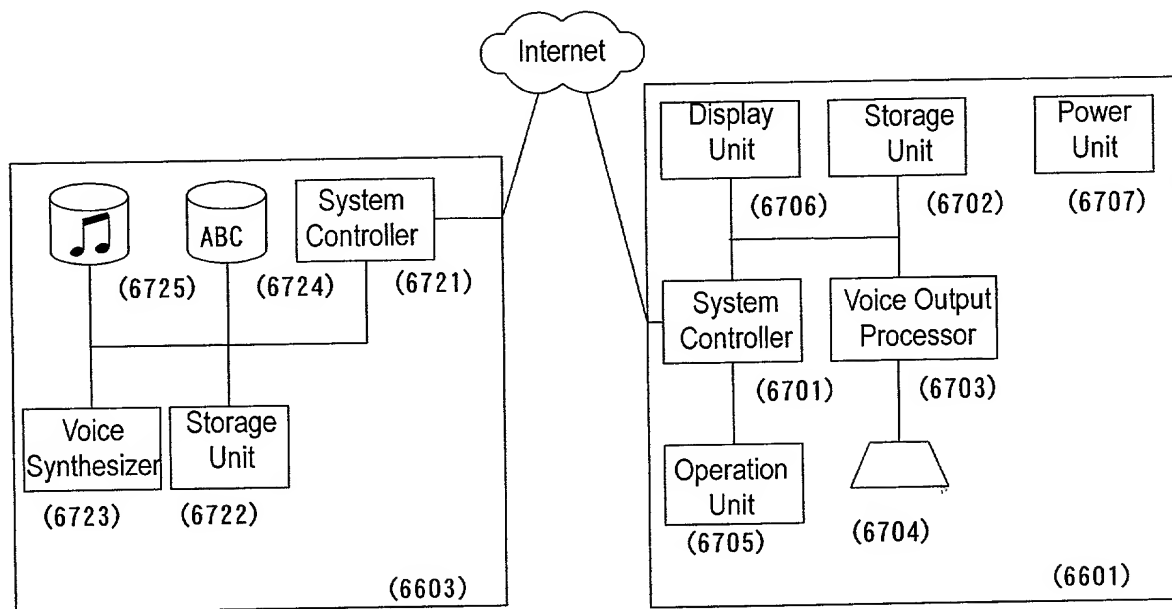


Fig. 68

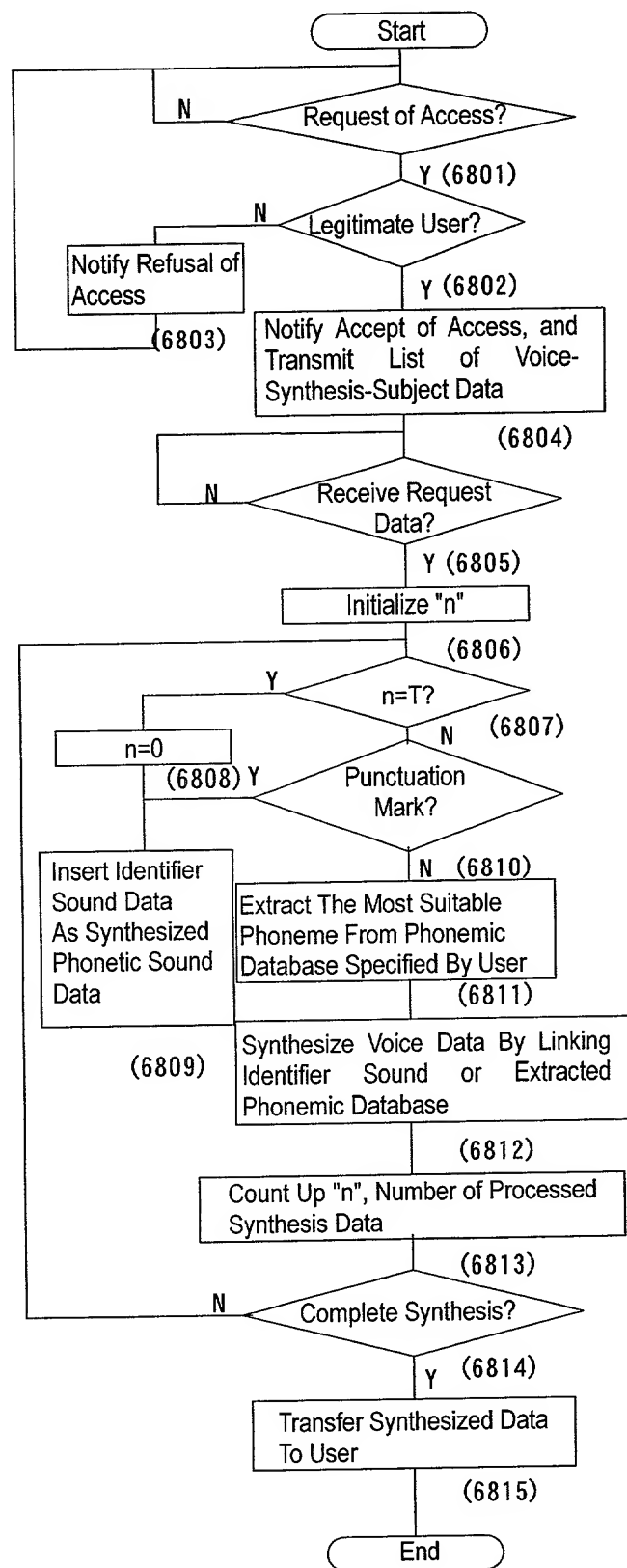


Fig. 69

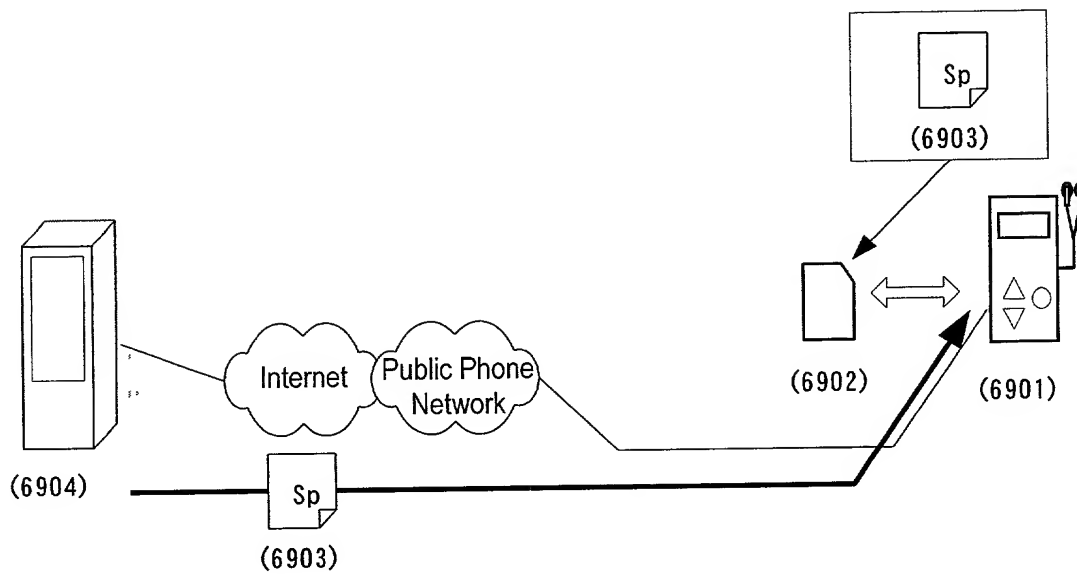


Fig. 70

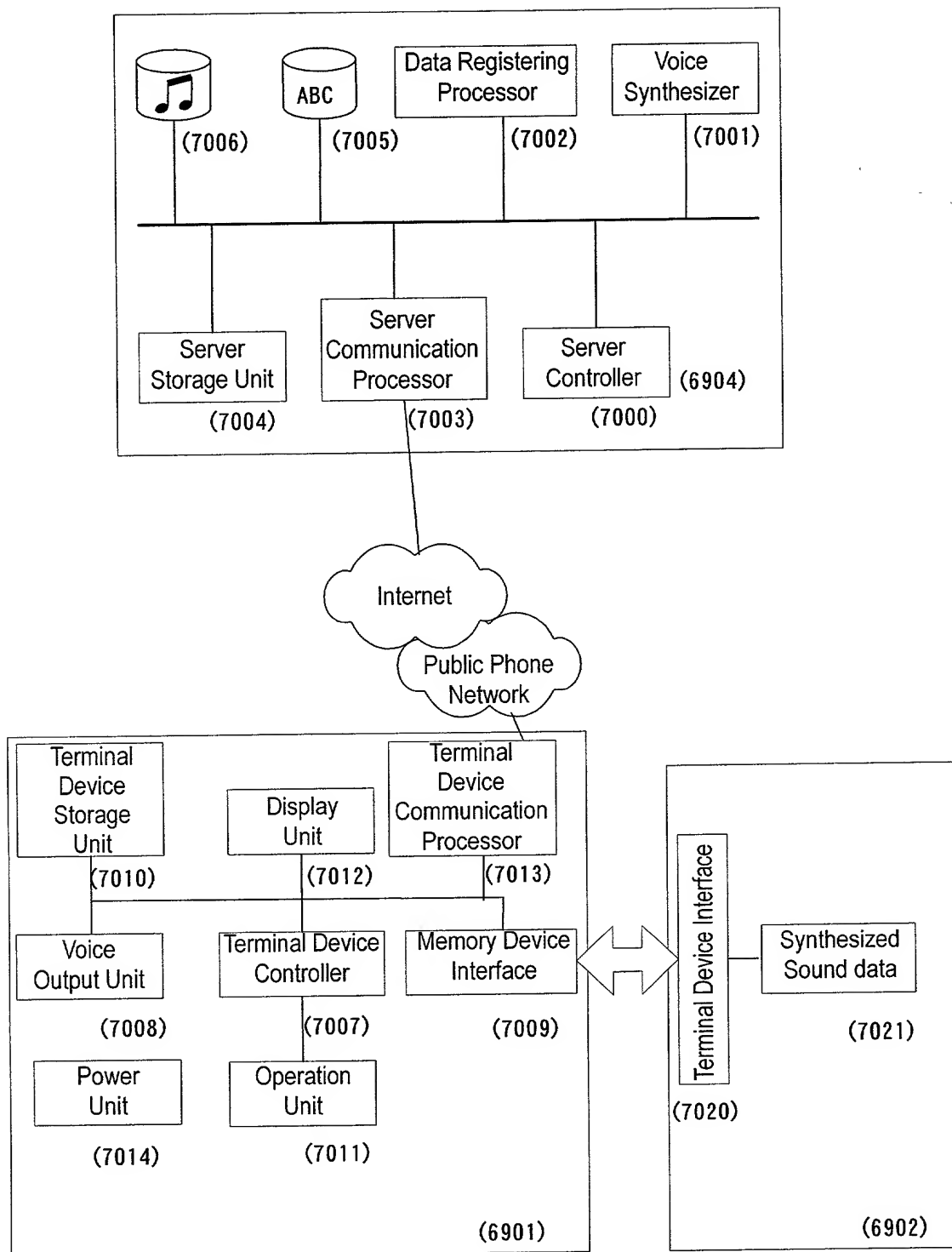


Fig. 71

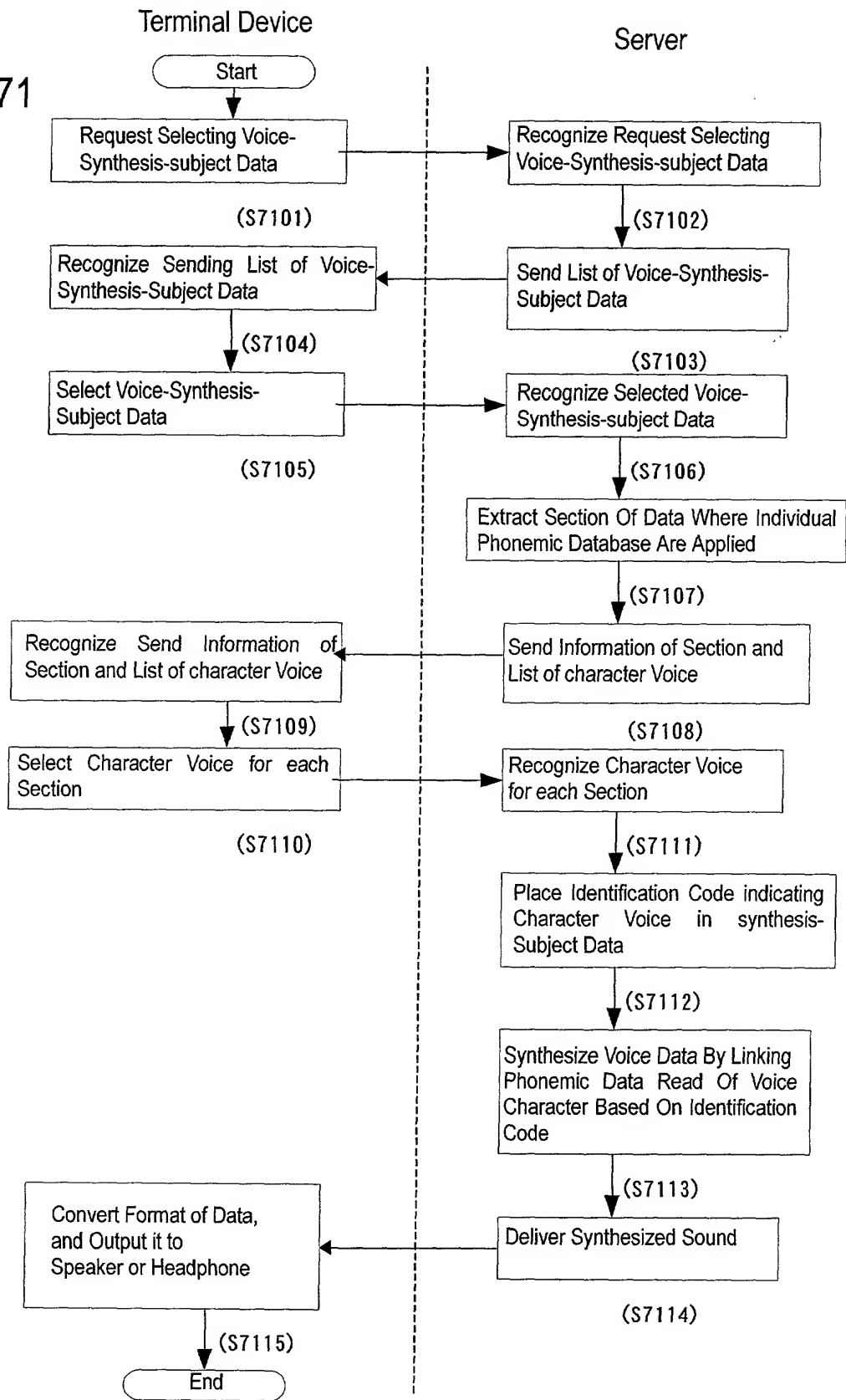


Fig. 72

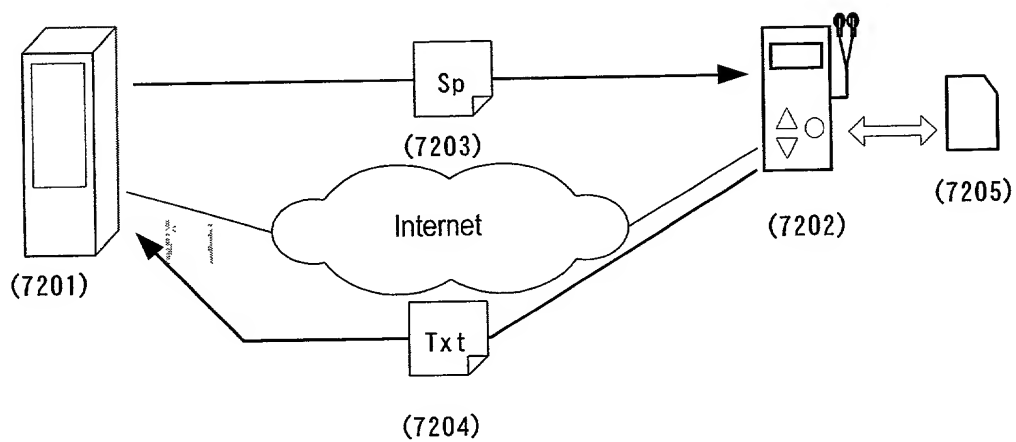


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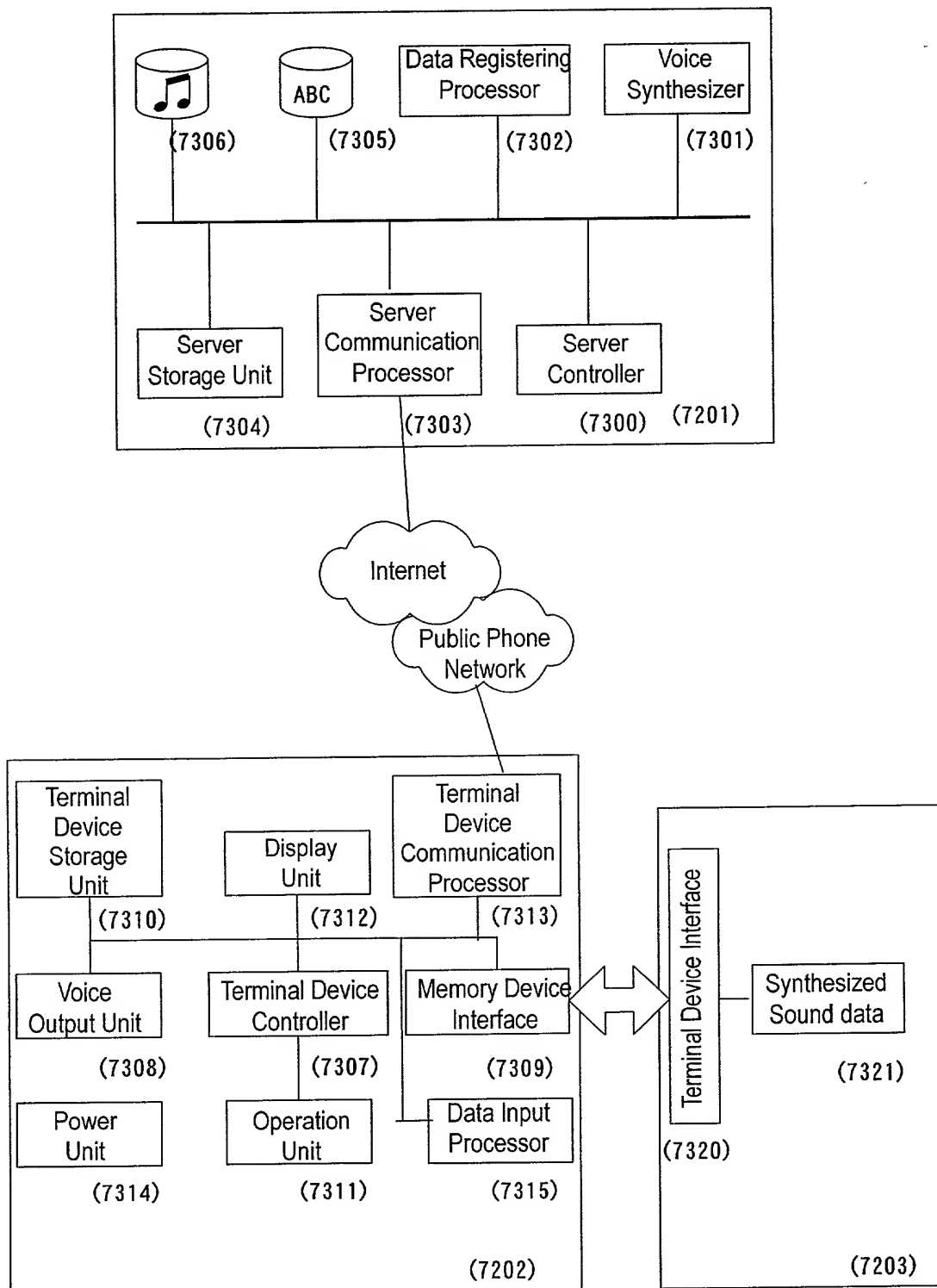


Fig. 74 Terminal Device

